



	Voltage Current 200 V 2.0 A			
DO-214AA (SMB)	FEATURES • Glass Passivated Junction Technology • Low profile package • Ideal for automated placement • Low power losses, high efficiency • High surge current capability • Cavity-free glass passivated junction • Low forward voltaje drop • Solder dip 260 °C, 10s • AEC-Q101 qualified • Component in accordance to RoHS 2011/65/EU and WEEE 2002/96/EC • Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C • Very soft recovery characteristics • Significantly reduced EMI. Very low Noise.			
	 MECHANICAL DATA Case: do-214AA (SMB). Epoxy meets UL 94V-0 flammability rating. Polarity: Color band denotes cathode end. Terminals: Matte tin plated leads, solderable per MIL-STD-750 Method 2026, J-STD-002 and JESD22-B102. Consumer grade, meets JESD 201 class 1A whisker test. HE3 suffix for high reliability grade, meets JESD 201 class 2 whisker test. TYPICAL APPLICATIONS Used in high frecuency rectification and freewheeling application in switching mode converters and inverters for consumer, computer, automotive and telecommunication. 			

Maximum Ratings and Electrical Characteristics at 25 °C

		FES2DSR	
Marking Code		vo	
$V_{_{\mathrm{RRM}}}$	Maximum Recurrent Peak Reverse Voltage (V)	200	
V_{RMS}	Maximum RMS Voltage (V)	140	
V _{DC}	Maximum DC Blocking Voltage (V)	200	
I _{F (AV)}	Maximum Average Forward Rectified Current at $T_{\rm C} = 110 ^{\circ}{\rm C}$ 2.0 A		
I _{FSM}	Peak Forward Surge Current, 8.3 ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	d) 100 A	
C _j	Typical Junction Capacitance (1MHz; -4V)	40 pF	
R _{th (j-c)}	Typical Thermal Resistance	20 °C/W	
R _{th (j-a)}	(5x5 mm ² x 130 μ Cooper Area)	60 ºC/W	
T_{j} T_{stg}	Operating Juction and Storage Temperature Range	- 65 to + 175 ^o C	





Static Electrical Characteristics

Symbol	Parameter	Test Cor	nditions	Max.	Unit
		T _j = 25 ºC	I _F = 2.0 A	0.90	
V _F	Max. Instantaneous Forward Voltage	T _j = 100 ºC	$I_{F} = 2.0 \text{ A}$	0.75	V
		T _j = 25 °C	$I_{F} = 0.7 \text{ A}$	0.80	
		T _j = 25 ºC	$V_R = V_{RR}$	5	
l _R	Max. DC Reverse Leakage Current	T _j = 100 ºC	$V_R = V_{RR}$	10	μА
		T _i = 175 ºC	$V_R = V_{RR}$	100	

Recovery Characteristics (Tj = 25 °C)

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Symbol	Test Conditions	Min.	Max.	Тур.	Unit
trr			25		
ta	$I_F = 0.5 \text{ A}$, $dI_F/dt = 100 \text{ A/}\mu\text{s}$, $Irr = 1000 \text{ mA}$			15	ns
tb				6	
tb/ta	Softness	0.45			
	$VR = 30V$, $dIF/dt = 50 A/\mu s$, $I_F = 1A$			15	
Qrr	$VR = 30V, dIF/dt = 50 A/\mu s, I_F = 2A$			20	nC
	$VR = 30V$, $dIF/dt = 50 A/\mu s$, $I_F = 5A$			25	
	$VR = 30V, dIF/dt = 50 A/\mu s, I_F = 20A$			60	





Ordering information

PREFERRED P/N	PACKAGE CODE	DELIVERY MODE	BASE QUANTITY	UNIT WEIGHT (g)
FES2DSR TRTB	TRTB	13" diameter tape and reel	3,200	0.0823
FES2DSR HE3 TRTB	TRTB	13" diameter tape and reel	3,200	0.0823

Package Outline Dimensions: (mm) DO- 214AA (SMB)

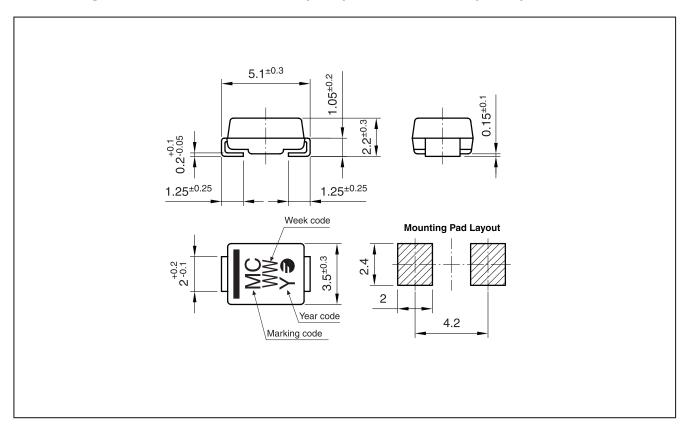






Fig. 1 REVERSE CURRENT vs REVERSE VOLTAGE

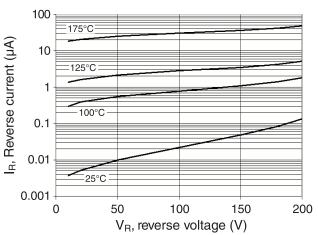


Fig. 2 FORWARD VOLTAGE vs FORWARD CURRENT

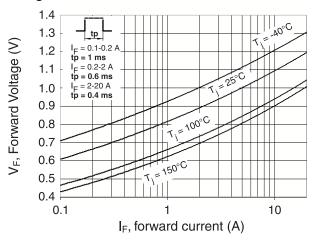


Fig. 3 LOW FREQUENCY POWER LOSSES vs. AVERAGE CURRENT

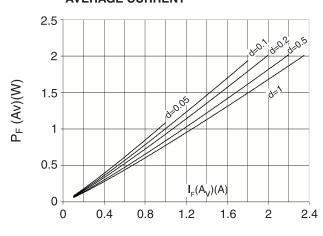


Fig. 4 PEAK CURRENT vs. FORM FACTOR

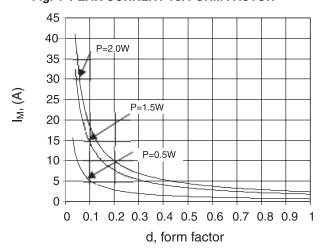


Fig. 5 RELATIVE VARIATION OF THERMAL IMPEDANCE TO AMBIENT vs. PULSE

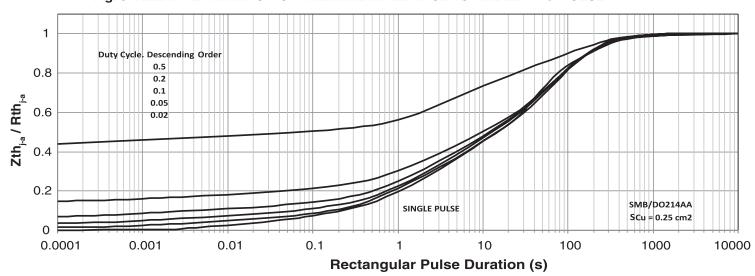




Fig. 6 FORWARD CURRENT DERATING CURVE

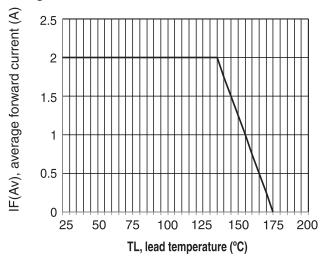


Fig. 7 tb/ta CURVES vs. FORWARD CURRENT

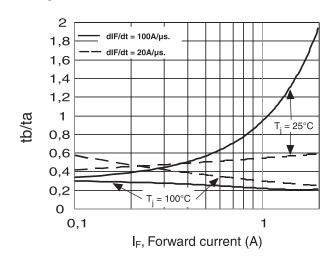


Fig. 8 t_{rr} , t_a AND t_b CURVES vs FORWARD CURRENT. Tc = 25 °C

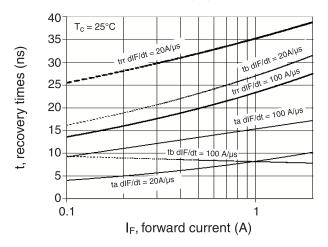


Fig. 9 t_{rr} , t_a AND t_b CURVES vs FORWARD CURRENT. T_C = 100 °C

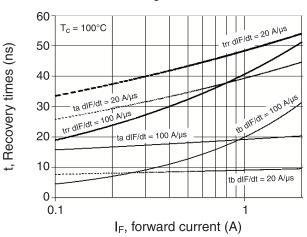


Fig. 10 RECOVERY TIME vs dI_F/dt

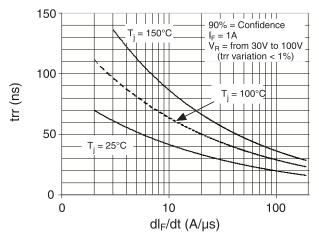


Fig. 11 PEAK REVERSE CURRENT vs dl_F/dt

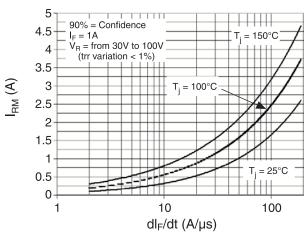




Fig. 40 tours of left 1 0 A

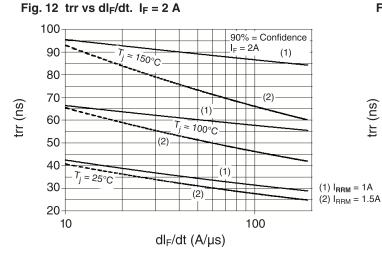


Fig. 13 trr vs dI_F/dt . $I_F = 0.1 A$

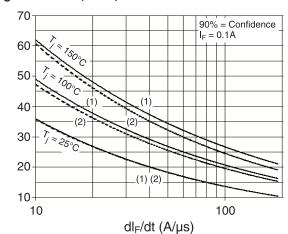


Fig. 14 trr vs dI_F/dt . $I_F = 0.5 A$

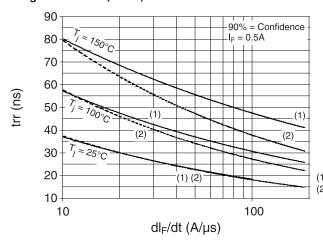


Fig. 15 QRR vs JUNCTION TEMPERATURE

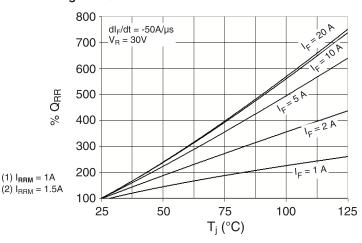


Fig. 16 IRM vs JUNCTION TEMPERATURE

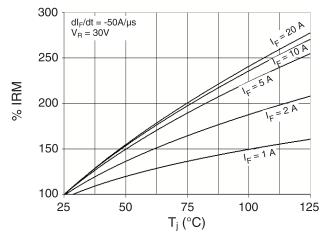


Fig. 17 trr vs JUNCTION TEMPERATURE

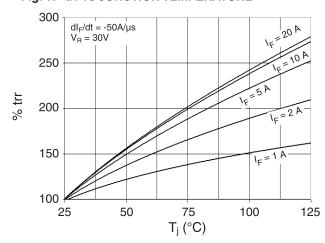






Fig. 18 TRANSIENT PEAK FORWARD VOLTAGE vs dl_F/dt

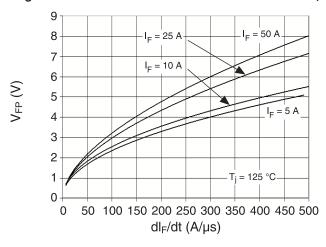


Fig. 19 FORWARD RECOVERY TIME vs dl_F/dt

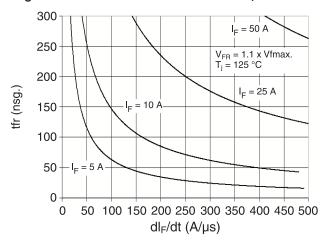


Fig. 20 RECOVERY TIME vs JUNCTION TEMPERATURE

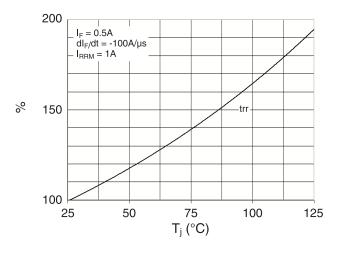
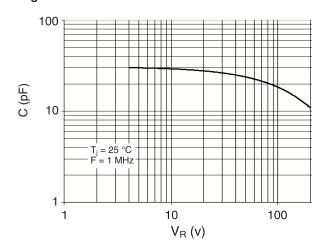


Fig. 21 JUNCTION CAPACITANCE vs. REVERSE BIAS







Revision History

DATE	REVISION	DESCRIPTION OF CHANGES
28-Aug-2019	0	Original Data Sheet
02-Dec-2019	1	Correct the Axis Label in Figure 5

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