

# Series AMSRB1-78JZ

## Up to 15 Watt | DC-DC Switching Regulator

#### **FEATURES:**

- · Switching Regulator
- Low Quiescent Current
- · Negative output available
- Non-Isolated
- Meet EN 62368 Standard
- SIP3 Package
- Efficiency Up To 96%
- Short Circuit Protection
- High MTBF
- RoHS Compliant



Picture Coming Soon

Models
Single output

		Output Voltage (V)	0 1 0 0	Efficiency	Efficiency
Model	Input Voltage (V)		Output Current max (mA)	Vin Max	Vin Min
				(%)	(%)
AMSRB1-783.3JZ	6-36	3.3	1000	80	90
AMSRB1-7805JZ	8-36	5	1000	85	93
AIVIORD 1-7000JZ	8-27	-5	-500	81	85
AMSRB1-7809JZ	13-36	9	1000	89	94
AMSRB1-7812JZ	16-36	12	1000	92	95
AWSKD1-7012JZ	8-20	-12	-300	87	88
AMSRB1-7815JZ	20-36	15	1000	93	96
	8-18	-15	-300	88	87

NOTE: All specifications in this datasheet are measured at an ambient temperature of 25°C, humidity<75%, for input voltage higher than 30 VDC, a 22uF/50V input capacitor is required. Nominal input voltage and at rated output load unless otherwise specified.

**Input Specifications** 

Parameters	Nominal	Typical	Maximum	Units
Voltage range	See Models table above			
Ouissant Current	Positive output	0.3	1	mA
Quiescent Current	Negative output	1	4	mA
Reverse Polarity Input	Prohibited			
Filter	Capacitor			

**Output Specifications** 

Parameters	C	Conditions		Maximum	Units
Valtaga agguragy	At 4000/ load	3.3V output	±2	±4	%
Voltage accuracy	At 100% load	Others	±1.5	±3	
Short Circuit protection		Continuo	us, hiccup mode		
Short circuit restart		Auto	o-Recovery		
Dynamic load stability	Nominal input volta	Nominal input voltage, 25% load step change		±200	mV
Transient recovery time	Nominal input volta	Nominal input voltage, 25% load step change		1	ms
Line voltage regulation	Vin=(LL-HL) at full	Vin=(LL-HL) at full load		±0.4	%
Lood voltage regulation	Nominal input, 109	6- Positive output	±0.4	±0.6	0/
Load voltage regulation	100% load	Negative output	±0.4	±0.8	%
Temperature coefficient	Full load	Full load		±0.03	%/°C
Ripple & Noise*	20MHz Bandwidth	20MHz Bandwidth, 20% to 100% load		75	mV p-p
Maximum Canacitiva Load	Positive output	Positive output		680	uF
Maximum Capacitive Load	Negative output	Negative output		330	ur

#### NOTE:

<sup>\*1.</sup> Ripple and noise tested with "parallel cable" method, please refer to DC-DC Converter Application Notes for specific operation methods;



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\*2. With the load lower than 20%, the maximum ripple and noise of 3.3V/5V output products will be 100mVp-p, 9V/12V/15V output products will be 2%Vo.

**General Specifications** 

Parameters	Conditions	Typical	Maximum	Units
Switching frequency	100% load	520		KHz
Operating temperature	With derating (see graph below)		+85	°C
Storage temperature	-55 to +125			°C
Maximum case temperature			100	°C
Cooling	Free Air Convection			
Humidity	Non-condensing		95	% RH
Case material	Plastic (UL94-V0)			
Weight	1.9			g
Dimensions (L x W x H)	0.46 x 0.31 x 0.41 Inches (11.6 x 8.0 x 10.4 mm)			
MTBF	> 2 000 000 hrs (MIL-HDBI	K-217F, Ground Beni	gn, t=+25 ₀C)	
Maximum Soldering Temperature	Welding time: 10s (Max.)		260	°C

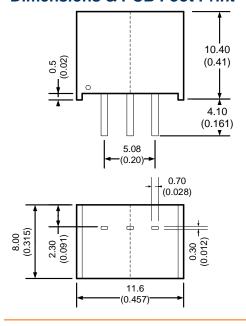
**Pin Out Specifications** 

Pin	Positive output	Negative output
1	+V input	+V input
2	Ground	-V output
3	+V output	Ground

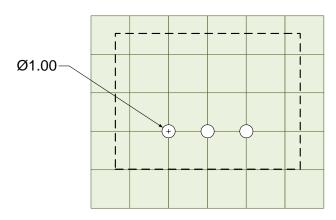
**Safety Specifications** 

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Parameters				
	Information Technology Equipment	Design to meet EN 62368		
	EMI - Conducted and radiated emission	CISPR32 / EN55032, class B (with the recommended EMI circuit)		
	Electrostatic Discharge Immunity	IEC 61000-4-2, Contact ±4KV, Criteria B		
Standards	RF, Electromagnetic Field Immunity	IEC 61000-4-3, 10V/m, Criteria A		
	Electrical Fast Transient / Burst Immunity	IEC 61000-4-4, ±1KV, Criteria B, with the recommended EMS circuit		
	Surge Immunity	IEC 61000-4-5, L-L ±1KV, Criteria B, with the recommended EMS circuit		
	RF, Conducted Disturbance Immunity	IEC 61000-4-6, 3 Vrms, Criteria A		

#### **Dimensions & PCB Foot Print**



F 051e R8.H



Grid: 2.54 x 2.54mm

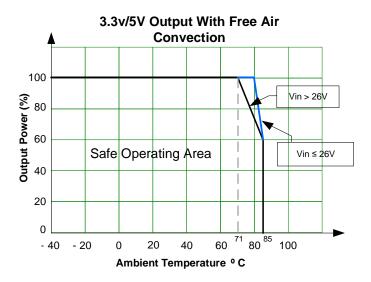
Unit:mm[inch]

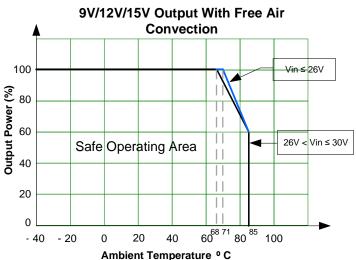
General tolerances:±0.5mm [± 0.020inch]

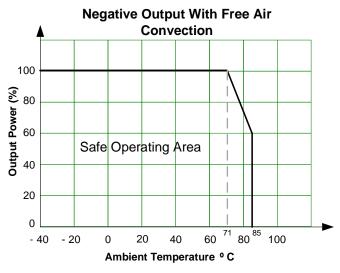


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### **Derating**

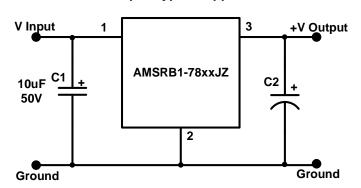




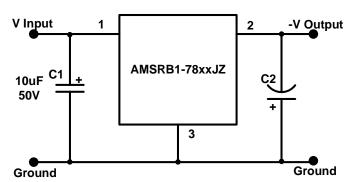


### **Application Circuit**

#### **Positive Output Typical Application Circuit**



#### **Negative Output Typical Application Circuit**

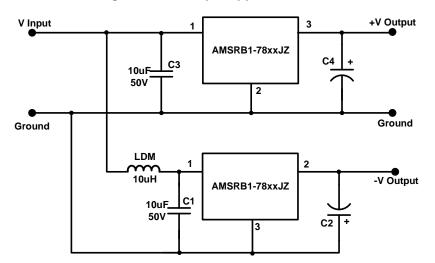


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#### Positive and Negative dual output application circuit

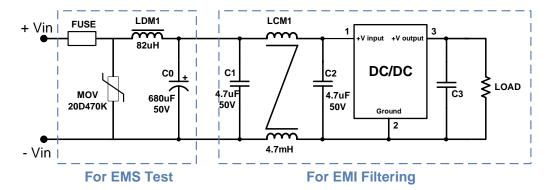


Model	C2/C4 (uF)	
3.3/5V output	22uF / 10V	
9V output	22uF / 16V	
12/15V output	22uF / 25V	

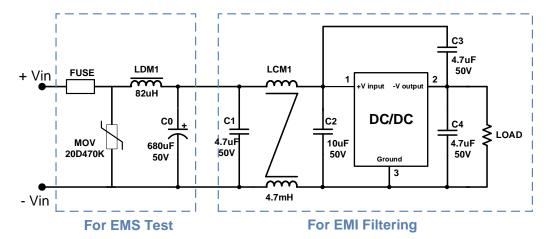
It is recommended that tantalum capacitor and aluminum electrolytic capacitor of low ESR capacitors are used for C2. C1/C3 & C2/C4 are required and should be installed as close to the converter as possible The converter cannot be used in parallel to enlarge the power for output and hot swap.

#### **EMC Recommended Circuits**

#### **Positive output**



### **Negative output**



The part choice of the FUSE is based on actual input current.

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