

Data Sheet N2513, REV.-

Technical Data

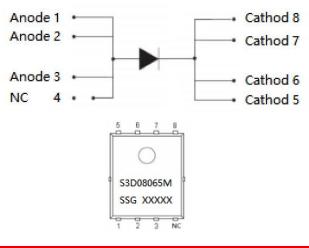
S3D08065M



S3D08065M 650V SIC POWER SCHOTTKY RECTIFIER



Circuit Diagram



Applications

- Alternative energy inverters
- Power Factor Correction (PFC)
- Free-Wheeling diodes
- Switching supply output rectification
- Reverse polarity protection

Description

S3D08065M is a SiC Schottky rectifier packaged in DFN5*6 case. The device is a high voltage Schottky rectifier that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S3D08065M is ideal for energy sensitive, high frequency applications in challenging environments.

Features

- 175°C T_J operation
- Ultra-low switching loss
- Switching speeds independent of operating temperature
- Low total conduction losses
- High forward surge current capability
- High package isolation voltage
- Terminals finish: 100% Pure Tin
- Pb Free Device
- All SMC parts are traceable to the wafer lot
- Additional electrical and life testing can be performed upon request

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Maximum Ratings



Characteristics	Symbol	Condition	Max.	Units
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _{DC}	-	650	V
Average Rectified Forward Current	I _{F (AV)1}	@Tc=25°C	24	A
	IF (AV)2	@Tc=153°C	8	A
Repetitive Peak Forward Surge Current	I _{FRM1}	10ms, Half Sine pulse, T _C =25°C	37.5	А
	I _{FRM2}	10ms, Half Sine pulse, T_C =110°C	25.5	A
Peak One Cycle Non-Repetitive Surge Current	I _{FSM1}	10ms, Half Sine pulse, T_{C} =25°C	71	A
	I _{FSM2}	10ms, Half Sine pulse, T_C =110°C	60	A
Non-Repetitive Peak Forward Surge Current	I _{F,Max1}	10µs. Pulse, T _C =25°C	650	А
Non-Acpetitive reak rorward ourge ourient	I _{F,Max2}	10µs. Pulse, T _C =110°C	530	A
	P _{tot1}	T _J =25℃	79.0	W
Power Dissipation	P _{tot2}	T _J =110°C	34.2	W

Electrical Characteristics:

Characteristics	Symbol	Condition	Тур.	Max.	Units
Forward Voltage Drop*	V _{F1} @ 8A, Pulse, T _J = 25 °C		1.4	1.7	V
	V _{F2}	@ 8A, Pulse, T _J = 175 °C	1.7	2.4	V
Reverse Current*	I _{R1}	$@V_R = rated V_R$ T _J = 25 °C	3	20	uA
	I _{R2}	$@V_R = rated V_R$ T _J = 175 °C	30	200	uA
Junction Capacitance	Ст	VR=0V, Tj=25℃,f=1MHz	650	-	pF
Reverse Recovery Charge	Qc	I _F = 8A, di/dt = 500A/µs VR = 400 V, TJ =25°C	40.55	-	nC
Capacitance Stored Energy	Ec	$V_{R} = 400 V$	9.93	-	μJ

 $^{*}\,$ Pulse width < 300 $\mu s,\,$ duty cycle < 2%

Thermal-Mechanical Specifications:

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	TJ	-	-55 to +175	°C
Storage Temperature	T _{stg}	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	R _{θJC}	DC operation	1.9	°C/W

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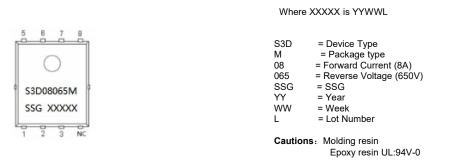
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Marking Diagram



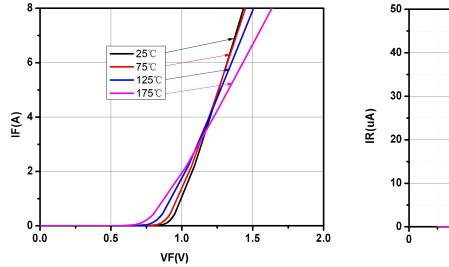


Ordering Information

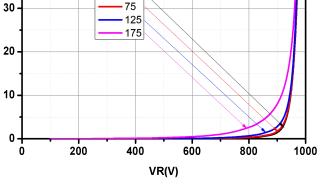
Device	Package	Shipping
S3D08065M	DFN 5*6	3000/Reel
S3D08065MTR	DFN 5*6	3000/Reel

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our tape and reel packaging specification.

Ratings and Characteristics Curves







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Fig.2-Typical Reverse Characteristics



S3D08065M



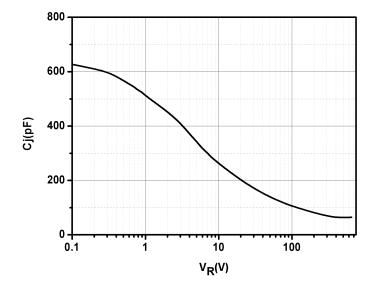


Fig.3-Capacitance vs. Reverse Voltage

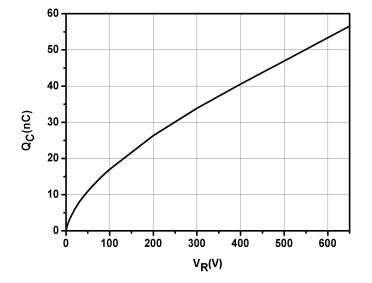


Fig.4-Total Capacitance Charge vs. Reverse Voltage

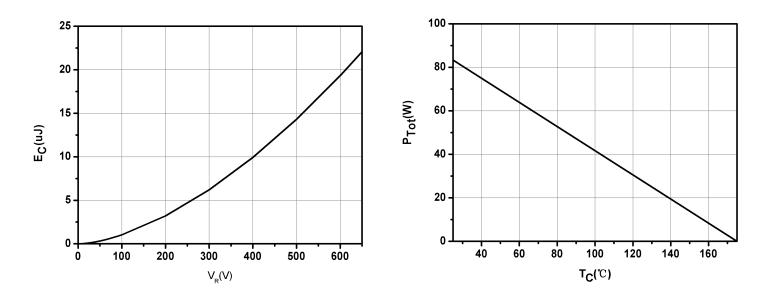


Fig.5-Capacitance Stored Energy

Fig.6-Power Derating



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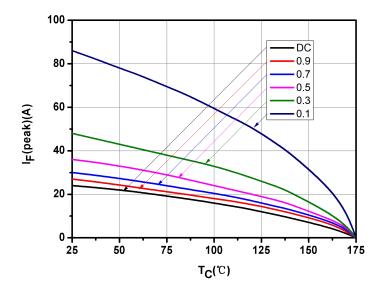
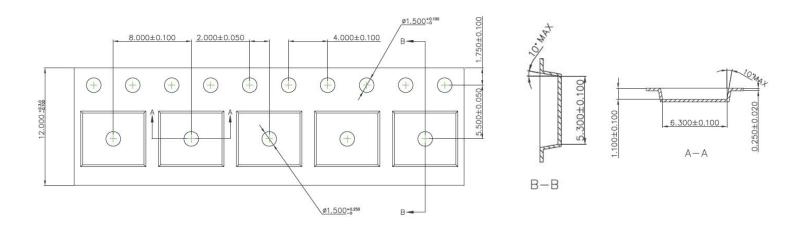


Fig.7-Current Derating

Carrier Tape & Reel Specification DFN5*6

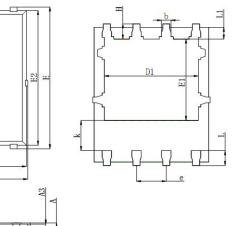




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D2 D

Mechanical Dimensions DFN5*6



SYMBOL	Millin	neters	Inches		
STMBOL	Min.	Max.	Min.	Max.	
A	0.900	1.000	0.035	0.039	
A3	0.254 REF.		0.010REF.		
D	4.944	5.096	0.195	0.201	
E	5.974	6.126	0.235	0.241	
D1	3.910	4.110	0.154	0.162	
E1	3.375	3.575	0.133	0.141	
D2	4.824	4.976	0.190	0.196	
E2	5.674	5.826	0.223	0.229	
k	1.190	1.390	0.047	0.055	
b	0.350	0.450	0.014	0.018	
е	1.270 TYP.		0.050 TYP.		
L	0.559	0.711	0.022	0.028	
L1	0.424	0.576	0.017	0.023	
Н	0.574	0.726	0.023	0.029	
Θ	10°	12°	10°	12°	

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