

RVT4.3A480272TFWN00

LCD TFT Datasheet

Rev.1.5

2015-02-06

| ITEM | CONTENTS | UNIT |
|--------------------------------|---------------------------------|-------------------|
| LCD Type | TFT/Transmissive/Normally white | / |
| Size | 4.3 | Inch |
| Viewing Direction | 12:00 (without image inversion) | O' Clock |
| Gray Scale Inversion Direction | 6:00 | O' Clock |
| LCM (W × H × D) | 106.30 ×68.00 × 3.40 | mm³ |
| Active Area (W × H) | 95.04 × 53.86 | mm² |
| Dot Pitch (W × H) | 0.066×0.198 | mm ² |
| Number Of Dots | 480 x (RGB) × 272 | / |
| Driver IC | ILI6480BQ | / |
| Backlight Type | 10 LEDs | / |
| Surface Luminance | 550 | cd/m ² |
| Interface Type | 24bit RGB | / |
| Color Depth | 16.7M | / |
| Pixel Arrangement | RGB Vertical Stripe | / |
| Surface Treatment | Anti-glare | |
| Input Voltage | 3.3 | V |
| With/Without TSP | Without Touch Panel | / |
| Weight | 63 | g |

Note 1: RoHS compliant

Note 2: LCM weight tolerance: ± 5%.

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REVISION RECORD

| REVNO. | REVDATE | CONTENTS | REMARKS |
|--------|------------|--|---------|
| 1.0 | 2014-05-16 | Initial Release | |
| 1.1 | 2014-11-24 | | |
| 1.2 | 2014-12-04 | Add HVDSL pin information | |
| 1.3 | 2015-01-19 | Update module thickness, update backlight information, update LED lifetime | |
| 1.4 | 2015-02-06 | Update dimensions information on mechanical drawing | |
| 1.5 | 2015-02-24 | Update Color Depth | |

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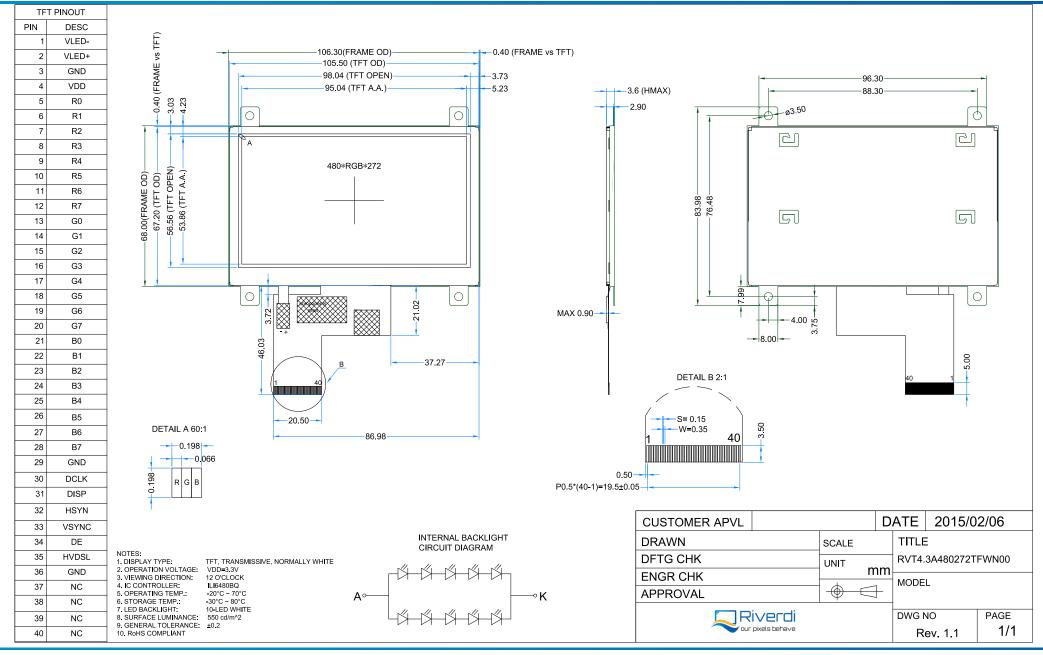
1 MODULE CLASSIFICATION INFORMATION

| RV | Т | 4.3 | А | 480272 | Т | F | W | Ν | 00 |
|----|----|-----|----|--------|----|----|----|----|-----|
| 1. | 2. | 3. | 4. | 5. | 6. | 7. | 8. | 9. | 10. |

| 1. | BRAND | RV – Riverdi |
|-----|------------------|-----------------------------------|
| 2. | PRODUCT TYPE | T – TFT Standard |
| ۷. | | F – TFT Custom |
| | | 3.5 – 3.5" |
| 2 | DISPLAY SIZE | 4.3 – 4.3" |
| 3. | DISTERT SIZE | 5.7 – 5.7" |
| | | 7.0 – 7.0" |
| 4. | MODEL SERIAL NO. | A (A-Z) |
| | | 320240 – 320x240 px |
| 5. | RESOLUTION | 480272 – 480x272 px |
| | | 800480 – 800x480 px |
| | | T – TFT LCD, RGB |
| 6. | INTERFACE | L – TFT LCD, LVDS |
| | | C – TFT + Controller |
| _ | FRAME | N – No Frame |
| 7. | FINAIVIL | F – Mounting Frame |
| 8. | BACKLIGHT TYPE | W – LED White |
| | | N – No Touch Panel |
| 9. | TOUCH PANEL | R – Resistive Touch Panel |
| | | C – Capacitive Touch Panel |
| 10. | VERSION | 00 (00-99) |

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3 ABSOLUTE MAXIMUM RATINGS

| PARAMETER | SYMBOL | MIN | MAX | UNIT |
|--------------------------------|-----------------|---------|----------------|------|
| Supply Voltage For Logic | VDD | -0.5 | 5.0 | V |
| Input Voltage For Logic | VIN | VSS-0.5 | VDD | V |
| LED forward current (each LED) | IF | - | 25 | mA |
| Operating Temperature | T _{OP} | -20 | 70 | °C |
| Storage Temperature | T _{ST} | -30 | 80 | °C |
| Humidity | RH | - | 90% (Max 60°C) | RH |

4 ELECTRICAL CHARACTERISTICS

| PARAMETER | SYMBOL | MIN | TYP | MAX | UNIT | NOTES |
|---------------------------|-----------------|--------|------|--------|------|------------|
| Supply Voltage For Module | VDD | 3.0 | 3.3 | 3.6 | V | |
| Input Current | IDD | - | 19.5 | TBD | mA | VDD = 3.3V |
| Input Voltage ' H ' level | V _{IH} | 0.7VDD | - | VDD | V | |
| Input Voltage ' L ' level | VIL | VSS | - | 0.2VDD | V | |

Note 1: The LED life time is defined as the module brightness decrease to 50% original brightness at Ta=25°C.

5 BACKLIGHT CHARACTERISTICS

| ITEM | SYMBOL | MIN | TYP | MAX | UNIT |
|---------------------------|--------|-------|-------|------|------|
| Voltage for LED backlight | Vı | 15.0 | 16.0 | 17.0 | V |
| Current for LED backlight | II | - | 40 | 50 | mA |
| LED Life Time | - | 30000 | 50000 | - | Hrs |

Note: The LED life time is defined as the module brightness decrease to 50% original brightness at Ta=25°C.

6 ELECTRO-OPTICAL CHARACTERISTICS

| ITEM | | SYMBOL | CONDITION | MIN | TYP | MAX | UNIT | REMARK | NOTE |
|-------------------------|---------------|---------|---------------|-------|-------|-------|-------------------|----------|------|
| Response Time Tr+Tf | | | - | 25 | 30 | ms | Figure 1 | 4 | |
| Contrast Ra | atio | Cr | θ=0° | 400 | 500 | - | | Figure 2 | 1 |
| Luminance Uniformity | | δ WHITE | Ø=0° Ta=25 | 80 | - | - | % | Figure 2 | 3 |
| Surface Lur | minance | Lv | - | - | 550 | - | cd/m ² | Figure 2 | 2 |
| | | | Ø = 90° | 40 | 50 | - | deg | Figure 3 | |
| | | θ | Ø = 270° | 60 | 70 | - | deg | Figure 3 | |
| Viewing An | Viewing Angle | | Ø = 0° | 60 | 70 | - | Deg | Figure 3 | 6 |
| Range | | | Ø = 180° | 60 | 70 | - | Deg | Figure 3 | U |
| | Red | Х | | 0.551 | 0.591 | 0.631 | | | |
| | | У | | 0.270 | 0.310 | 0.350 | | | |
| CIE (x, y) | Green | Х | θ=0° | 0.302 | 0.342 | 0.382 | | | |
| Chromati | | У | Ø=0° | 0.516 | 0.561 | 0.601 | | iguro 2 | |
| city | city Blue | Х | φ=0 Ta=25 | 0.105 | 0.145 | 0.185 | Figure 2 | | |
| | | У | 10-23 | 0.047 | 0.087 | 0.127 | | | 5 |
| White | White | х | | 0.250 | 0.290 | 0.330 | | | |
| | | У | | 0.300 | 0.340 | 0.380 | | | |

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Note 1. Contrast Ratio(CR) is defined mathematically as below, for more information see Figure 1.

Contrast Ratio =
$$\frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see Figure 2.

$$\delta \text{ WHITE } = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope series.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Figure 3.

Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

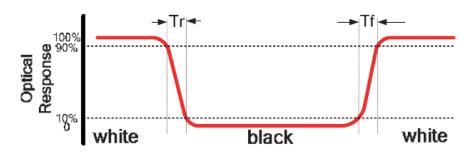


Figure 1. The definition of response time

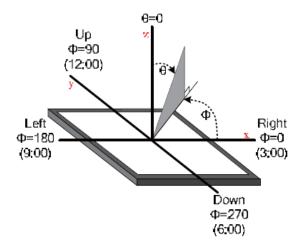


Figure 2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A: 5 mm
B: 5 mm
H,V: Active Area
Light spot size Ø=5mm, 500mm distance from the
LCD surface to detector lens
measurement instrument is TOPCON's luminance
meter BM-5

P1
P2
P4
P3

Figure 3.The definition of viewing angle



7 INTERFACE DESCRIPTION

| | 0/04501 | | |
|---------|---------|-----------------------------------|--------|
| PIN NO. | SYMBOL | DESCRIPTION | NOTE |
| 1 | VLED- | Cathode Of LED Backlight | |
| 2 | VLED+ | Anode Of LED Backlight | |
| 3 | GND | Power Ground | |
| 4 | VDD | Power Voltage | |
| 5-12 | R1-R7 | Red Data (R1-LSB, R7-MSB) | |
| 13-20 | G1-G7 | Green Data (G1-LSB, G7-MSB) | |
| 21-28 | B1-B7 | Blue Data (B1-LSB, B7-MSB) | |
| 29 | GND | Power Ground | |
| 30 | DCLK | Pixel Clock | |
| 31 | DISP | Display On/Off | |
| 32 | HSYN | Horizontal Sync Signal | |
| 33 | VSYN | Vertical Sync Signal | |
| 34 | DE | Data Enable | |
| 35 | HVDSL | HV mode or DE mode control signal | Note 1 |
| 36 | GND | Power Ground | |
| 37 | NC | No Connect | |
| 38 | NC | No Connect | |
| 39 | NC | No Connect | |
| 40 | NC | No Connect | |

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Note 1: Displays marked with "rev.2.0" printing, have the ability to select the operating mode: HV mode or DE mode.

HVDSL="H": Set under HV mode, VSD and HSD signal have to provide by system.

HVDSL="L": Set under DE mode, DE signal have to provide by system.

By default: Internal pulled weak low.

8 LCD TIMING CHARACTERISTICS

8.1 Clock and data input time diagram

(DE Mode)

DCLK

DR[7:0]

DB[7:0]

DB[7

Figure 4. Clock and data input time diagram

8.2 Parallel RGB input timing table

| PARAMETER | SYMBOL | MIN | TYP | MAX | UNIT |
|------------------|--------|-----|-----|-----|------|
| DCLK Frequency | Fclk | 5 | 9 | 12 | MZH |
| VSD Period Time | Tv | 277 | 288 | 400 | Н |
| VSD Display Area | Tvd | | 272 | | Н |
| VSD Back Porch | Tvb | 3 | 8 | 31 | Н |
| VSD Front Porch | Tvfp | 2 | 8 | 97 | Н |
| HSD Period Time | Th | 520 | 525 | 800 | DCLK |
| HSD Display Area | Thd | | 480 | | DCLK |
| HSD Back Porch | Thbp | 36 | 40 | 255 | DCLK |
| HSD Front Porch | Thfp | 4 | 5 | 65 | DCLK |



9 RELIABILITY TEST

| NO. | TEST ITEM | TEST CONDITION |
|-----|----------------------------|---|
| 1 | High Temperature Storage | 80±2°C/240hours |
| 2 | Low Temperature Storage | -30±2°C/240hours |
| 3 | High Temperature Operating | 70±2°C/240hours |
| 4 | Low Temperature Operating | -20±2°C/240hours |
| 5 | Temperature Cycle | -30±2°C~25~80±2°C × 20 cycles (30min.) (5min.) (30min.) |
| 6 | Damp Proof Test | 60°C ±5°C × 90%RH/240hours |
| 7 | Vibration Test | Frequency 10Hz~55Hz Amplitude of vibration: 1.5mm Sweep: 10Hz~55Hz~10Hz X, Y, Z 2 hours for each direction. |
| 8 | Package Drop Test | Height:60 cm 1 corner,3 edges,6 surfaces |
| 9 | ESD Test | Air: ± 4 KV 150 pF/ 330Ω 5 times Contact: ± 2 KV 150 pF/ 330Ω 5 time |

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