SPECIFICATIONS

CUSTOMER .

SAMPLE CODE : SH128800T004-ZFC

MASS PRODUCTION CODE . PH128800T004-ZFC

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 004

DRAWING NO. (Ver.) . LMD-PH128800T004-ZFC (Ver.004)

PACKAGING NO. (Ver.) . PKG-PH128800T004-ZFC (Ver.002)

Customer Approved

Date:

Approved	Checked	Designer
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Preliminary specification for design input

Specification for sample approval

2017.07.21 TW RD APR

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History of Version

Date	Ver.	Edi.	Description	Page	Design by
03/13/2017	01	001	New Drawing.	-	Stephen
03/21/2017	01	002	Update Spec	15 \ 16	Stephen
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					2/
				<i></i>	

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LCM Packaging Specifications



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Touch panel	Projective Capacitive Touch Panel
Touch panel	USB HID Touch
Screen size(inch)	10.1(Diagonal)
Driver element	IPS,Normally Black
Resolution	1280* (R · G · B) * 800 Dots
Display mode	Transmissive, ANTI-GLARE
Color	16.7M
Weight	458.7 g
Interface	HDMI
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website:
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	254.96(W) * 173.6 (L) * 26.2 (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	216.96 (W) * 135.60 (L)	mm

Note: For detailed information please refer to LCM drawing.



1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	VDD		-0.3	+4.0	V
Operating Temperature	Тор	-	-30	+80	$^{\circ}\!\mathbb{C}$
Storage Temperature	T _{ST}	-	-30	+80	$^{\circ}\!\mathbb{C}$
Storage humidity	H _D	Ta<25 °C	55	60	%RH

1.4 DC Electrical Characteristics

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Power Supply Volt LCD Driver	_	VDD-VSS	-	3.0	3.3	3.6	V	-
Power Supply Volt LED Driver	•	VLED-VLSS	-	6	12	21	٧	-
PWM Signal	High	V _{PWM}		3.0	-,	3.6	V	-
Voltage	Low	V PWM		0	-/	0.4	V	-
LED Enable	High	V. 50. 511		3.0	-	3.6	V	-
Voltage	Low	VLED_EN		0	-	0.4	V	-
VDD Currer	it	IDD	VDD=3.3V, Pattern= Picture*1	ı	0.295	1	Α	-
VLED Curre	nt	ILED	VLED=12V PWM=100%	-	0.2	-	Α	-
VDD Power Consu	umption	PDD	VDD=3.3V	-	-	1.2	W	-
VLED Powe Consumptio		PLED	VLED=12V	-	-	2.5	W	
Rush Currer	nt	Irush	-	-	-	1.5	Α	-
Driver Ripple Vo	Itage	VVDD-RP	-	-	-	300	mV	-
				100	-	200		D ым≥0.1%
				200	-	500		D ым≥0.25%
				500	-	1000		D ым≥0.5%
Input PWM Frequency		FPWM	_	1000	-	2000	Hz	Dым≥1%
		I I VVIVI	-	2000	-	5000	1 12	D ым≥2.5%
				5000	-	10000		Опм≥5%
				10000	-	20000		D ым≥10%
				20000	-	30000		D ым≥15%

Note1: Maximum current display.



1.5 Optical Characteristics

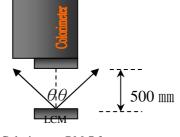
TFT LCD Panel Ta=25 ℃

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	-
Response tim	пе	Tr + Tf	-	-	25	50	ms	Note2
	Тор	ΘΥ+		75	85	-		
Viouing angle	Bottom	ΘΥ-	CR ≥ 10	75	85	-	Dog	Note4
Viewing angle	Left	ΘХ-	CH ≥ 10	75	85	1	Deg.	Note4
	Right	ΘX+		75	85	-		
Contrast rati	0	CR		600	800	-	1	Note3
	White	X		0.265	0.315	0.365		
	vvriite	Υ		0.303	0.353	0.403		
0 1 (0)5	Dod	Х		-	0.591	-		
Color of CIE	Red	Υ	IF=TBD mA	-	0.349	-		Natad
Coordinate (With B/L)	Green	X		-	0.344	-	-	Note1
(**************************************	arcon	Υ		-	0.581	1		
	Blue	X) -	0.154	-		
	blue	Υ		-	0.146	-		
Average Brightr Pattern=white di		IV	IF=80 mA	350	450	-	cd/m2	Note1
Luminance unifo	rmity	YU	IF=80 mA	70	75	-	%	Note1

Note1:

- $1 : \triangle B=B(min) / B(max) \times 100\%$
- 2 : Measurement Condition for Optical Characteristics:
 - a: Environment: 25°C±5°C / 60±20%R.H → no wind → dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 \pm 50 mm \rightarrow (θ = 0°)
 - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





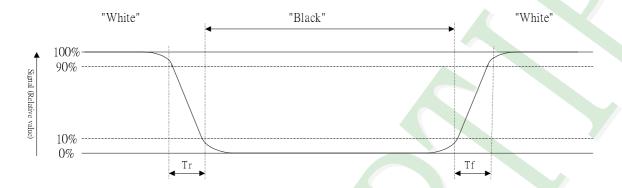
Colorimeter=BM-7 fast



Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



Note3: Definition of contrast ratio:

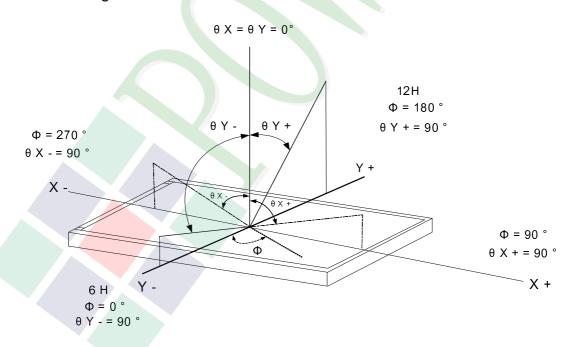
Contrast ratio is calculated with the following formula

Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle: Refer to figure as below:





1.6 Backlight Characteristics

Maximum Ratings

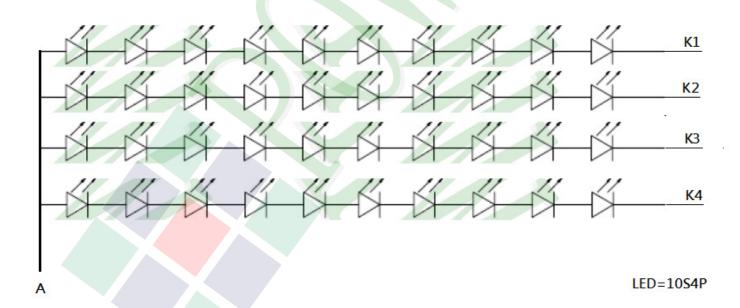
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Power Dissipation	Pd	-	-	100	-	mW
LED Forward Current	IF	1 LED	-	<u>-</u>	30	mA
LED Reverse Voltage	VR	1 LED	- 4	A-	1.2	V

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Voltage for LED Backlight	VF	If=80mA	27.5	31	34	V
Current for LED Backlight	IF	II=0UIIIA	-	80	\	mA
Color	White					

Other Description

Item	Conditions	Description
Life Time	Ta =25°C If= 80 mA	50000 hrs



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1.7 Touch Panel Characteristics

Features

Item	Standard Value
Touch Panel Size	10.1"
Touch type	Projective capacitive touch panel
Input Method	Finger / 5 Points touch
Output Interface	USB
IC	mxT1066T

Mechanical Specifications

Item	Standard Value	\searrow	Unit
Viewing Area	217.96 (W) * 136.60 (L)		mm

Absolute Maximum Ratings

	3				
ltem	Symbol	Condition	Min.	Max.	Unit
Supply voltage	VDD_5.0	-	-0.3	+6.0	٧
Operating Temperature	Тор	-	-30	+80	.€
Storage Temperature	Tst	-	-30	+80	ç

DC Electrical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply Voltage	VDD_5.0	-	-	5.0	-	\

Touch Panel IC Read/Write description & Register Mapping

Reference: Atmel Touch Driver Porting Reference Guide.



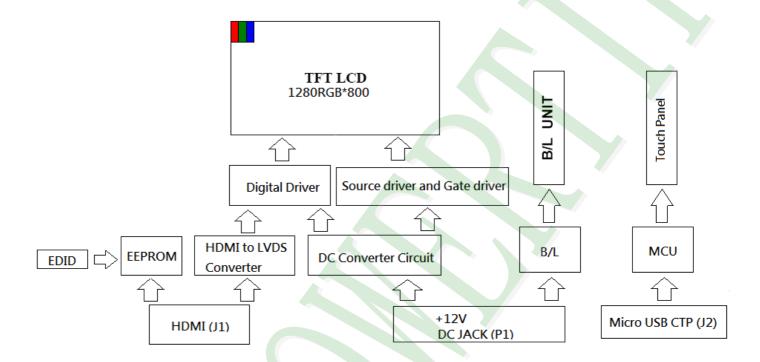
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

2.2.1 (J1:HDMI 1.3 A type Interface)

Pin#	Name	Description
1	TX2+	TMDS Data 2+
2	TX2 Shield	TMDS Data 2 Shield
3	TX2-	TMDS Data 2-
4	TX1+	TMDS Data 1+
5	TX1 Shield	TMDS Data 1 Shield
6	TX1-	TMDS Data 1-
7	TX0+	TMDS Data 0+
8	TX0 Shield	TMDS Data 0 Shield
9	TX0-	TMDS Data 0-
10	TXC+	TMDS Clock+
11	TXC Shield	TMDS Clock Shield
12	TXC-	TMDS Clock-
13	CEC	CEC
14	NC	No connection
15	SCL	Serial Clock for DDC
16	SDA	Serial Data for DDC
17	GND	Power Ground
18	V5V	+5V Power for HDMI
19	Hot Plug Detect	Hot Plug Detect



2.2.2 (J2:Micro USB Capacitive Touch Panel Interface)

Pin#	Name	Description
1	V5V	V _{Bus} 4.4V-5.25V
2	D-	Data-
3	D+	Data+
4	ID	No connection
5	GND	Power Ground.

2.2.3 (J7:LVDS Interface)

Pin#	Name	Description
1	V3.3	+3.3V Power
2	V3.3	+3.3V Power
3	V3.3	+3.3V Power
4	GND	Power Ground
5	GND	Power Ground
6	GND	Power Ground
7	TX0-	LVDS Data 0-
8	TX0+	LVDS Data 0+
9	TX1-	LVDS Data 1-
10	TX1+	LVDS Data 1+
11	TX2-	LVDS Data 2-
12	TX2+	LVDS Data 2+
13	GND	Power Ground
14	GND	Power Ground
15	TXCLK-	LVDS Clock -
16	TXCLK+	LVDS Clock+
17	TX3-	LVDS Data 3-
18	TX3+	LVDS Data 3+
19	GND	Power Ground
20	GND	Power Ground



2.2.4 (J8:POWER Interface)

Pin#	Name Description	
1	GND	Power Ground
2	GND	Power Ground
3	NC	No connection
4	B/L Enable	Backlight Enable(Active High)
5	V12V	+12V Power
6	V12V	+12V Power

2.2.5 (PJ1:POWER DC JACK Interface)





Hold Φ6.4mm / Center Pin Φ 2.0mm

Pin#	Name	Description
1	V12V	+12V Power
2	GND	Power Ground

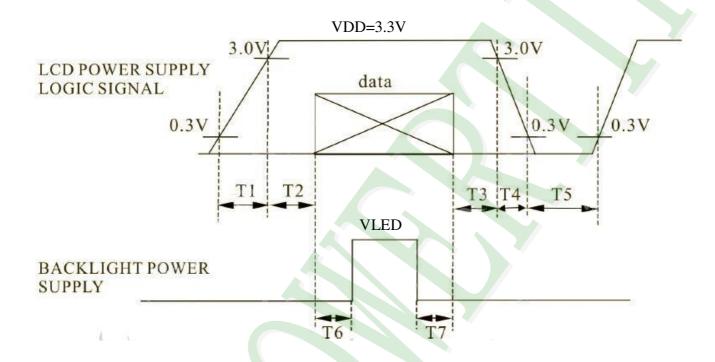


2.3 Timing Characteristics

2.3.1 POWER ,SIGNAL SEQUENCE

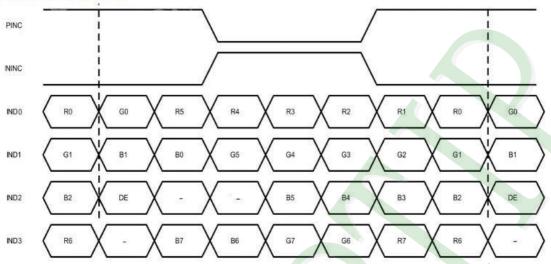
0.5<t1≤10ms 200ms≤t5 0<t2≤50ms 200ms≤t6 0<t3≤50ms 200ms≤t7

0<t4≤10ms





2.3.2 LVDS Data Input Format 8-BIT LVDS INPUT



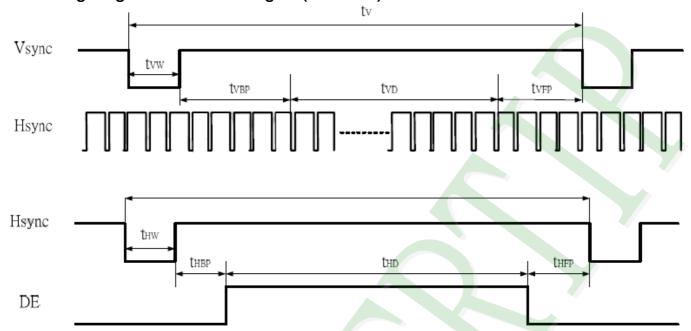
2.3.3 Interface Timings

Parameter	Symbol	Unit	Min.	Тур.	Max.
Frame Rate	-	Hz		60	-
Frame Period	Tv	line	815	823	1023
Vertical Display Time	TVD	line		800	
Vertical Blanking Time	Tvw+Tvbp+Tvfp	line	15	23	33
1 Line Scanning Time	Тн	clock	1410	1440	1470
Horizontal Display Time	THD	clock	b.	1280	
Horizontal Blanking Time	THW+THBP+THFP	clock	60	160	190
Clock Rate	1/Tc	MHz	68.9	71.1	73.4





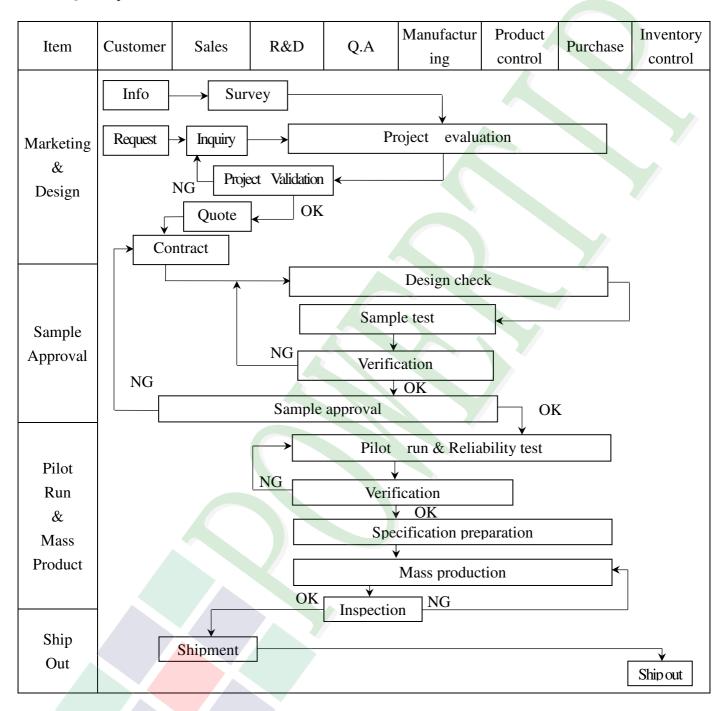
2.3.4 Timing Diagram of Interface Signal (DE mode)



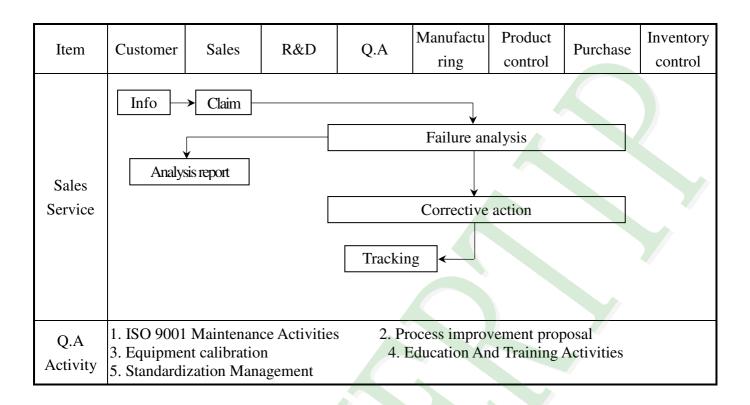


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2. Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3, 5" ~10" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge · MIL-STD · Powertip Tester · Sample

◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

♦OUT Going Defect Level: Sampling.

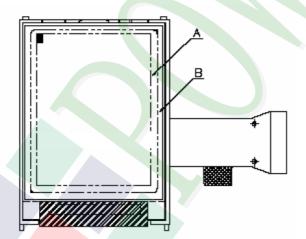
◆Standard of the product appearance test:

a. Manner of appearance test:

- (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



$\spadesuit Specification For TFT-LCD Module 3. 5"~10":$

NO	Item	Criterion	Level	
		1. 1The part number is inconsistent with work order of production.	Major	
01	01 Product condition	1. 2 Mixed product types.	Major	
		1. 3 Assembled in inverse direction.	Major	
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major	
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major	
		4. 1 Missing line character and icon.	Major	
	04 Electrical Testing		4. 2 No function or no display.	Major
04		4. 3 Display malfunction.		
		4. 4 LCD viewing angle defect.		
		4. 5 Current consumption exceeds product specifications.	Major	
		Item Acceptance (Q'ty)		
	Dot defect	Bright Dot ≤ 4		
	Dot defect	Dot Dark Dot ≤ 5		
	(Bright dot \	$\begin{array}{ c c c } \hline \textbf{Defect} & \textbf{Joint Dot} & \leq 3 \\ \hline \end{array}$		
05	Dark dot)	Total ≤ 7	Minor	
	On -display	5. 1 Inspection pattern: full white, full black, Red, Green and		
		blue screens.		
	 5. 2 It is defined as dot defect if defect area >1/2 dot. 5. 3 The distance between two dot defect ≥5 mm. 			
		o, o the distance between two dot defect \(\geq 0\) min.		



igspace Specification For TFT-LCD Module 3. 5" ~10":

NO	Item	Criterion	Level
		6. 1 Round type (Non-display or display):	
		Dimension (diameter : Φ) Acceptance (Q'ty) A area B area	
	Black or white dot \(\) scratch \(\)	$\Phi \le 0.25$ Ignore	
	contamination	$0.25 < \Phi \leq 0.50$ 5 Ignore	
	Round type	$\Phi > 0.50$	
	Y	Total 5	
06	$\Phi = (x+y)/2$	6. 2 Line type(Non-display or display) :	Minor
	Line type	Length (L) Width (W) Acceptance (Q'ty)	
		A area B area	
	→ V W	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	
	L	$L \leq 5.0 \qquad 0.05 < W \leq 0.10 \qquad \qquad 2 \qquad \qquad Ignore$	
		W >0.10 As round type	
		Total 5	
		Dimension (diameter : Φ) Acceptance (Q'ty)	
		$\Phi \le 0.25$ Ignore B area	
07	Polarizer Bubble	$0.25 < \Phi \leq 0.50$	Minor
		$0.50 < \Phi \leq 0.80$ 1 Ignore	14111101
		$\Phi > 0.80$	
		Total 5	



◆Specification For TFT-LCD Module 3. 5″ ~10″:

NO	Item	Criterion		Level
		Z: The thickness of crack	Y : The width of crack. W : terminal length a : LCD side length	
		8. 1 General glass chip: 8. 1. 1 Chip on panel surface and cra	ack between panels:	
08	The crack of glass	SP Z	Z Y SP	Minor
		Y [OK] Seal width	(NG)	
		X Y	Z	
		≤ a Crack can't enter viewing area	≤1/2 t	
*		≤ a Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	



igspace Specification For TFT-LCD Module 3. 5" ~10":

NO	Item	Criterion					
	Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8. 1. 2 Corner crack:						
		$egin{array}{ c c c c c }\hline X & Y & Z \\ & \leq 1/5 & a & Crack can't enter & Z & \leq 1/2 t \\ & & viewing area & Z & \leq 1/2 t \\ \hline \end{array}$					
		≤1/5 a Crack can't exceed the half of SP width. $1/2$ t < Z $≤2$	t				
08	The crack of glass	8.2 Protrusion over terminal:					
		8.2.1 Chip on electrode pad: W X X X X X X X X X X X X	Įz f				
		X Y Z					
		Front $\leq a$ $\leq 1/2 \mathrm{W}$ $\leq t$					
		Back $\leq a$ $\leq W$ $\leq 1/2$	t				



◆Specification For TFT-LCD Module 3. 5″~10″:

NO	Item	Criterion		
	Item			
08	The crack of glass	X Y Z ≤ 1/3 a ≤ W ≤t ∴ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode	Minor	
		terminal specifications. 8. 2. 3 Glass remain : $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		



4. RELIABILITY TEST

4.1 Reliability Test Condition

4.	neliability lest collu						
NO.	TEST ITEM	TEST CONDITION					
1	High Temperature Storage Test	Keep in +80 ±2℃ 96 hrs Surrounding temperature, then storage at normal condition 4hrs.					
2	Low Temperature Storage Test	Keep in −30 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.					
3	High Temperature / High Humidity Storage Test	Keep in +60℃ / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)					
4	Temperature Cycling Storage Test	$-30^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow +80^{\circ}\text{C} \rightarrow +25^{\circ}\text{C}$ $(30\text{mins}) (5\text{mins}) (5\text{mins})$ 10 Cycle Surrounding temperature, then storage at normal condition 4hrs.}					
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: $15^{\circ} \sim 35^{\circ} \sim 15^{\circ} \sim 1$					
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min) The amplitude of vibration :1.5 mm Each direction (X \cdot Y \cdot Z) duration for 2 Hrs 					
7	Drop Test (Packaged)	Packing Weight (Kg) Drop Height (cm) 0 ~ 45. 4 122 45. 4 ~ 90. 8 76 90. 8 ~ 454 61 Over 454 46 Drop direction : ※1 corner / 3 edges / 6 sides each 1 times					



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

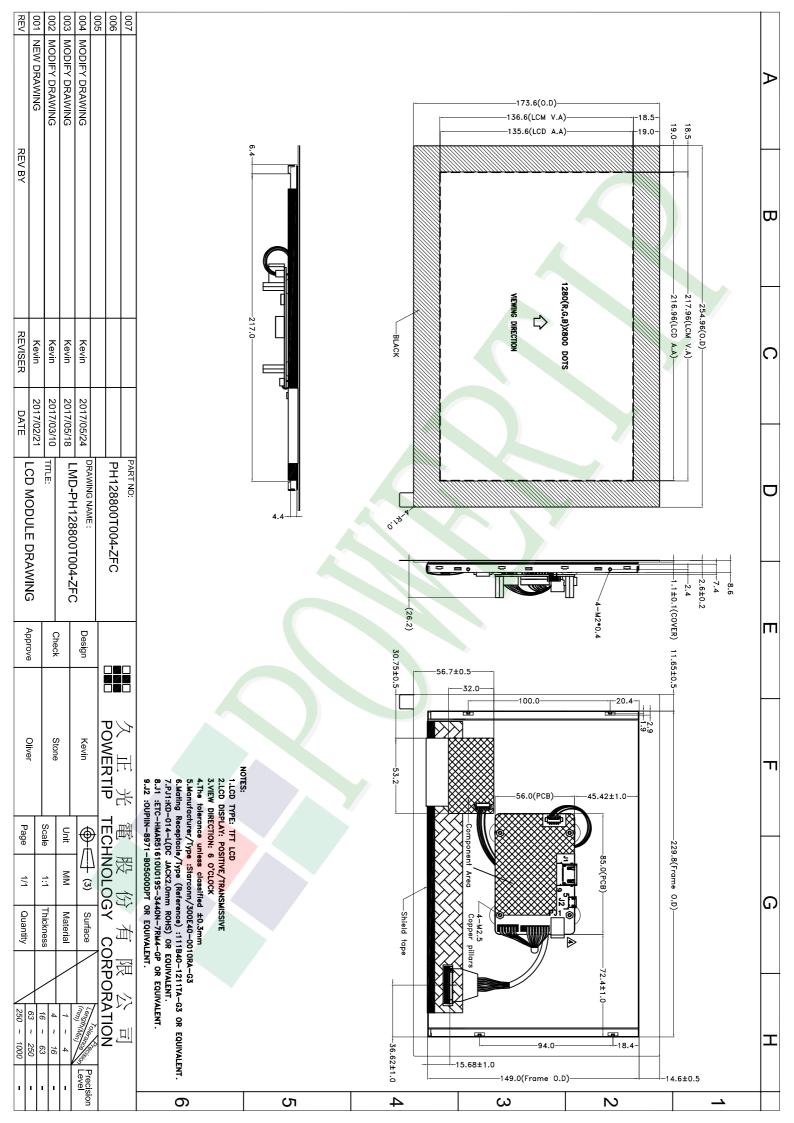
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Ver.002	ICME		Approve	Check	Contact					
LCM 已表外指言										
Documents NO. PKG-PH128800T004-ZFC LCM Packaging Specifications (For Tray)			S Oliver	Stone	Kevin					
(For Tray) 1.包裝材料規格表 (Packaging Material): (per carton)										
No. Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight					
	PH128800T004-ZFC	254.96 X 173.6	0.4587	Quality 8	3.6696					
1 成品 (LCD) 2 多層薄膜(1)POF	OTFILM0BA03ABA	234.90 X 173.0	0.4387	4	3.0090					
2 多層為族(1)TOT 3 TRAY 盤 (2)Tray	TY00000000425	352 X 260 X 35.8	0.15	12	1.8					
4 内盒(3)Product Box	BX38327211AABA	383 X 272 X 110	0.13	4	1.0					
5 保利龍板(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284	2	0.0568					
6 外紙箱(5)Carton	BX57041027CCBA	570 X 410 X 265	1.4208	1	1.4208					
7 舒美墊(6)EPE	FOAM00000047	350 X 255 X 5	0.011	8	0.088					
8										
9										
2.一 整箱總重量 (Total LCD Weigh	t in carton): 8.04 Kg±1	10%								
3.單箱數量規格表 (Packaging Speci		was of two								
(1)LCD quantity per box : no per tra (2)Total LCD quantity in carton : qu		x no of tray x no of boxes	2	= 2 = 8						
Use empty tray 空盤 (4)保利龍板										
2 盤			(4)保利龍板 Polylon board	>						
	(4) A = +*P+*									
	(1)多層薄膜————————————————————————————————————									
+	101			$\swarrow \searrow$						
Det and least into the trans										
Put products into the tray			\rightarrow							
(2)TRAY 盤 ——	(4)伊利	等据 人	\bigvee	$\overline{}$					
	Tray	(4)保利 Polylon	月E11次 board							
	(6)舒美墊			₩ _						
	EPE			^						
	(6)舒美墊	(5)/2	小紙箱 🗸							
Tray stacking	EPE EPE		arton							
Tray stacking										
A										
B	(3)内盒 Product Box		0		PARTIES. POR SECTION S					
			PO	WERTIP						
	特記事	項 (REMARK)	<u> </u>							
A 斜角 Detail	В									
Total Total										
	Tray 2									
圆円/	Tray 1									
4. TRAY盤相疊時,需旋轉180度,請詳見B視圖										
Rotate tray 180 degrees and place on top of stack.										
Check the tray stack using Fig. B.										
			LID TE							