

	SPECIFICATION	5			
CUSTOMER	· PTC				
SAMPLE CODE	NSC1602	_RU-JWA-K			
MASS PRODUCTION CODE	NPC1602	_RU-JWA-K			
SAMPLE VERSION	. 01				
SPECIFICATIONS EDITION	001				
DRAWING NO. (Ver.)	: JLMD-NP	C1602LRU-JWA-K_001			
PACKAGING NO. (Ver.)	PACKAGING NO. (Ver.) : JPKG-NPC1602LRU-JWA-K_001				
		Date: JS RD APPROVED			
Approved	Checked	Date: 2014.06.18			
Approved 閆偉	Checked 劉進	Date: 2014.06.18			
	劉進 n for design input	Date: 2014.06.18 US RD APPROVED Designer			
閏偉 □ Preliminary specification ■ Specification for sample	劉進 n for design input e approval <b>OWERTIP TECH. CO</b>	Date: Designer 周志仙			



# History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
06/13/2014	01	001	New Sample	-	周志仙
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## Contents

- **1. SPECIFICATIONS** 
  - 1.1 Features
  - **1.2 Mechanical Specifications**
  - 1.3 Absolute Maximum Ratings
  - **1.4 DC Electrical Characteristics**
  - **1.5 Optical Characteristics**
  - **1.6 Backlight Characteristics**
- 2. MODULE STRUCTURE
  - 2.1 Counter Drawing
  - 2.2 Interface Pin Description
  - 2.3 Timing Characteristics
  - 2.4 Display Command
  - 2.5 Character Pattern
  - 2.6 Jumper (Setting different use)
- **3. QUALITY ASSURANCE SYSTEM** 
  - 3.1 Quality Assurance Flow Chart
  - 3.2 Inspection Specification
- 4. RELIABILITY TEST
  - 4.1 Reliability Test Condition

## **5. PRECAUTION RELATING PRODUCT HANDLING**

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty

## Appendix :

- 1. LCM drawing
- 2. PKG drawing

Note : For detailed information please refer to IC data sheet : SITRONIX---ST7066U-0A



### **1. SPECIFICATIONS**

#### 1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	STN Y/G , Positive , Transflective
Driver Condition	LCD Module : 1/16 Duty , 1/5 Bias
Viewing Direction	12 O'clock
Weight	32.2g
Interface	6800-series 8-bit parallel
Driver IC	ST7066U
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

## **1.2 Mechanical Specifications**

Item	Standard Value	Unit
Outline Dimension	85.0 (L) * 30.0 (W) *12.7(H)	mm
Viewing Area	66.0 (L) * 16.0 (W)	mm
Active Area	56.2 (L) * 11.5 (W)	mm
Character Size	2.95mm * 5.55mm	mm
Character Pitch	3.55mm * 5.95mm	mm

Note : For detailed information please refer to LCM drawing



### 1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V <sub>DD</sub>	-	-0.3	7.0	V
LCD Driver Supply Voltage	$V_{LCD}$	-	Vdd -10.0	VDD +0.3	V
Input Voltage	V <sub>IN</sub>	-	-0.3	V <sub>DD</sub> +0.3	V
Operating Temperature	T <sub>OP</sub>	-	-20	70	°C
Storage Temperature	T <sub>ST</sub>	-	-30	80	°C
Storage Humidity	$H_{D}$	Ta<60 ℃	-	90	%RH

## **1.4 DC Electrical Characteristics**

					Ta = 1	<b>25°</b> ℃
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	$V_{DD}$	-	4.5	5.0	5.5	V
"H" Input Voltage	VIH		0.7 Vdd	-	Vdd	V
"L" Input Voltage	V <sub>IL</sub>		-0.3	-	0.6	V
"H" Output Voltage	V <sub>OH</sub>	IOH=-0.1mA	3.9	-	Vdd	V
"L" Output Voltage	V <sub>OL</sub>	IOL=0.1mA	-	-	0.4	V
Supply Current	I <sub>DD</sub>	V <sub>DD</sub> = 5.0 V ,Vop= 4.5V Pattern= Horizontal *1	-	2.0	3.0	mA
	V <sub>OP</sub>	<b>-20</b> °C	4.4	4.6	4.8	
LCM Driver Voltage		<b>25</b> ℃	4.3	4.5	4.7	V
	*2	<b>70</b> ℃	4.1	4.3	4.5	

NOTE: \*1 The Maximum current display

\*2 The VOP test point is (VDD –V0)



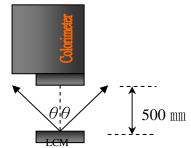
## **1.5 Optical Characteristics**

	LCD Panel	:1/16 Dut	ty,1/5 Bia	as,V <sub>LCD</sub> :	= 4.5V,	Ta =25℃		
Item		Symbol	Conditions	Min.	Тур.	Max.	Unit	Reference
	Rise	tr		-	80	125	ma	Note 2
Response Time	Fall	tf	-	-	220	330	ms	Note 2
	Тор	θ+		-	40	-		
Viewing angle	Bottom	θ-	0.2.0	-	40	-	Dec	Note 1
range	Left	θL	C <u>&gt;</u> 2.0	-	45	-	Deg	Note 1
	Right	θR		-	45	-		
Contrast Ra	tio	С	-	-	10	-	-	Note 3
Average Brightness (with LCD) *2 Wavelength (with LCD)		IV	IE-100 mA	30	35	-	cd/m <sup>2</sup>	
		λр	IF=100 mA	568	571	574	nm	Note 4
Uniformity *	1	∆B	IF=100 mA	70	-	_	%	

Note 4 :

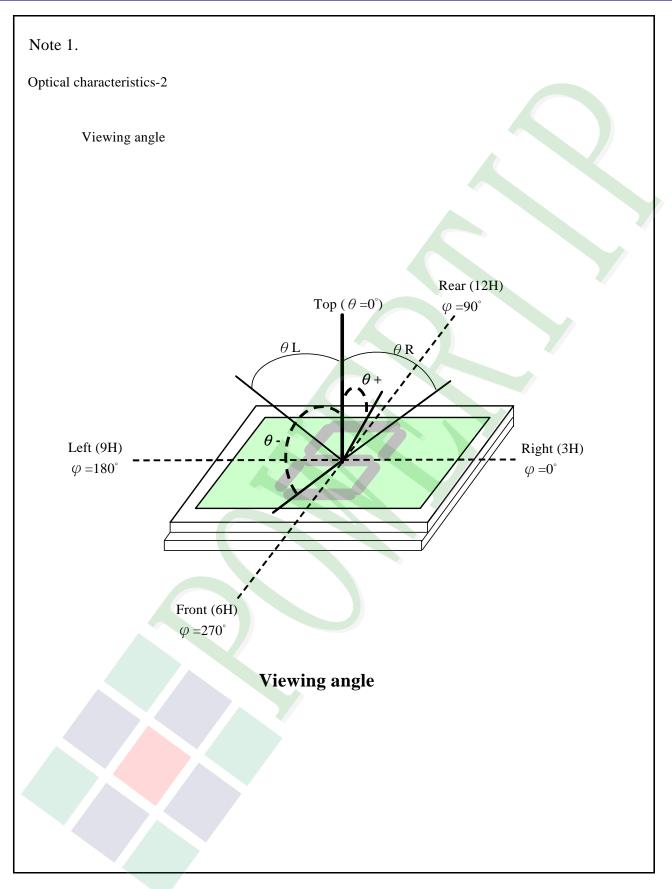
- 1 : △B=B(min) / B(max) \* 100%
- 2 : Measurement Condition for Optical Characteristics:
  - a : Environment: 25°C ±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
    - b : Measurement Distance: 500 ± 50 mm  $\rightarrow$  ( $\theta$ = 0°)
  - c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
  - d : The uncertainty of the C.I.E coordinate measurement  $\pm 0.01$  , Average Brightness  $\pm 4\%$



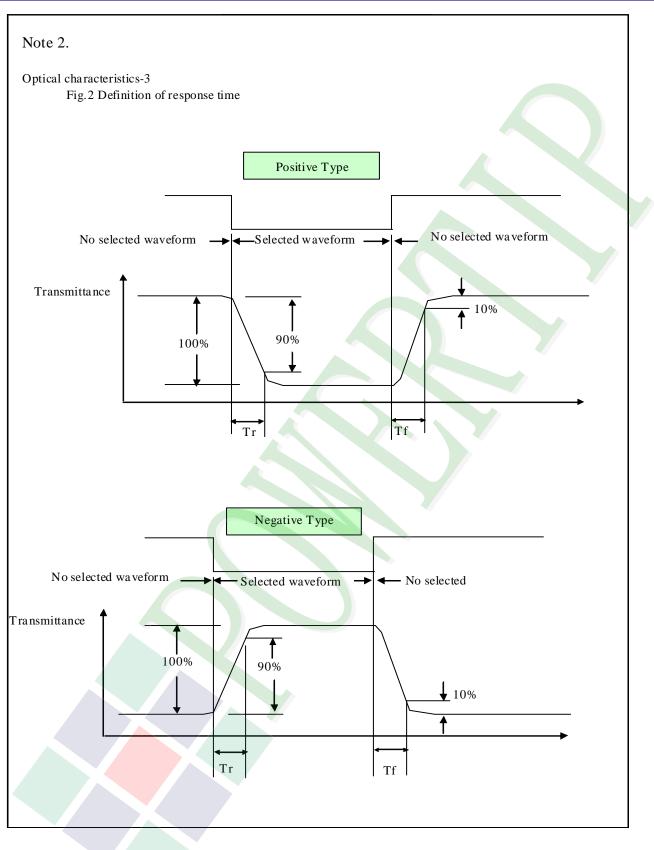


Colorimeter=BM-7 fast

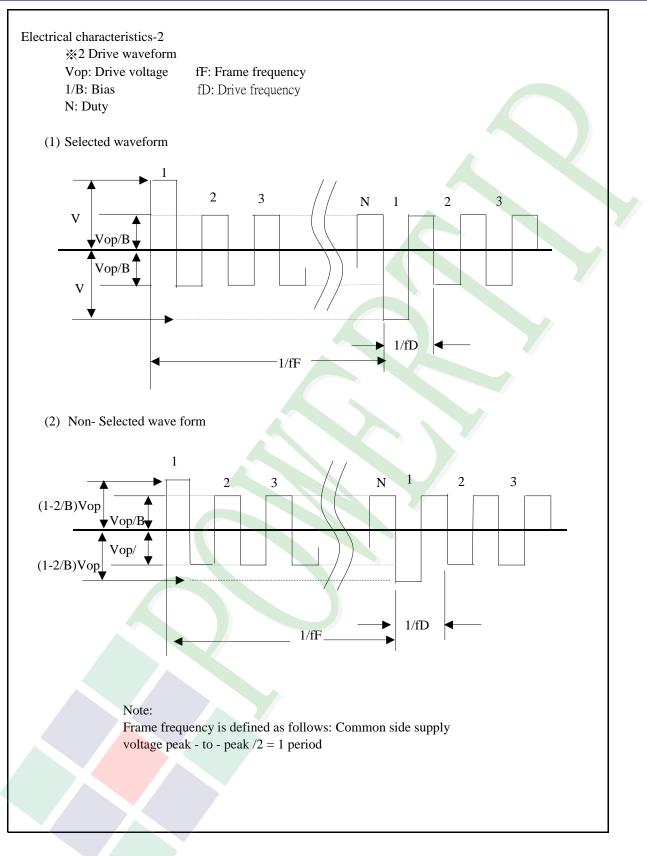




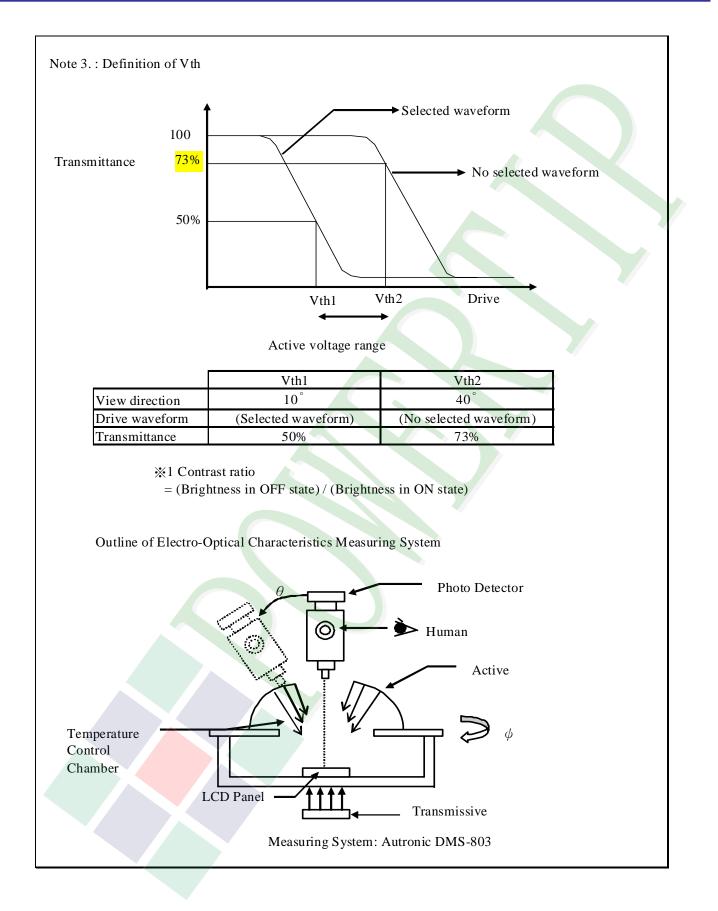














## 1.6 Backlight Characteristics

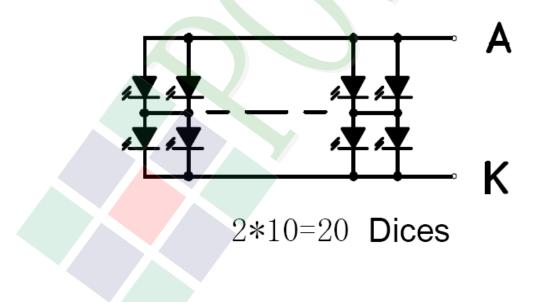
#### Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	<b>Ta =25</b> ℃	-	150	mA
Reverse Voltage	VR	Ta =25℃	-	8	V
Power Dissipation	PD	<b>Ta =25</b> ℃	-	660	mW

#### Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Forward Voltage	VF		4.0	4.2	4.4	V	
Reverse Current	IR		-	-	100	uA	
Average Brightness (without LCD)	IV	IF= 100 mA	165	190	-	cd/m <sup>2</sup>	
Wavelength (Without LCD)	λρ		569	572	575	nm	
Color	Yellow/Green						

Internal Circuit Diagram:





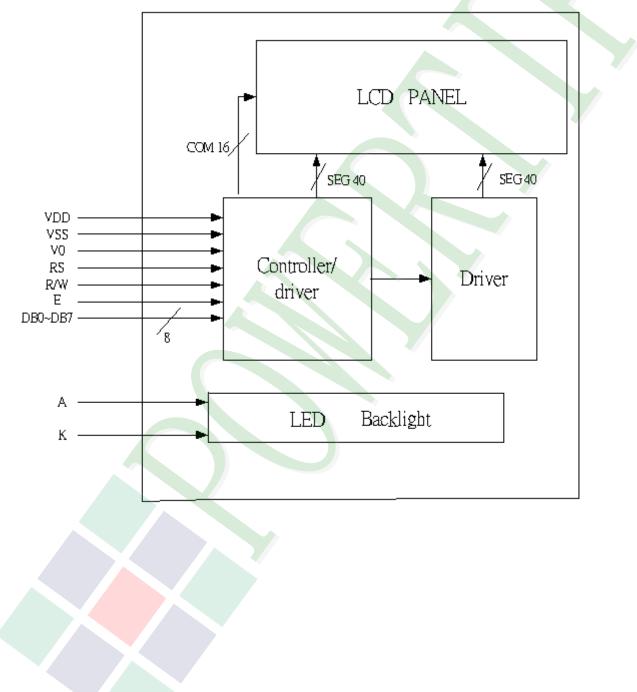
# 2. MODULE STRUCTURE

## 2.1 Counter Drawing

#### 2.1.1 LCM Mechanical Diagram

\* See Appendix

#### 2.1.2 Block Diagram



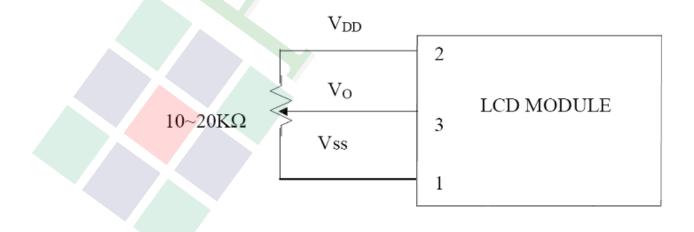


## 2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	V <sub>SS</sub>	Power Supply (Vss=0)
2	V <sub>DD</sub>	Power Supply (5V)
3	Vo	Operating voltage for LCD
4	RS	Register Selection input High = Data register Low = Instruction register (for write) Busy flag address counter (for read)
5	R/W	Read/Write signal input is used to select the read/write mode High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
7	DB0	Four low order bi directional three state data hus lines. Les for
8	DB1	<ul> <li>Four low order bi-directional three-state data bus lines. Use for</li> <li>data transfer between the MPU and the LCD module.</li> </ul>
9	DB2	These four are not used during 4-bit operation.
10	DB3	These four are not used during 4-bit operation.
11	DB4	
12	DB5	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module.
13	DB6	DB7 can be used as a busy flag.
14	DB7	

#### 2.2.1 Application Notes

Contrast Adjust





#### 2.2.2 Refer Initial code

void initial()

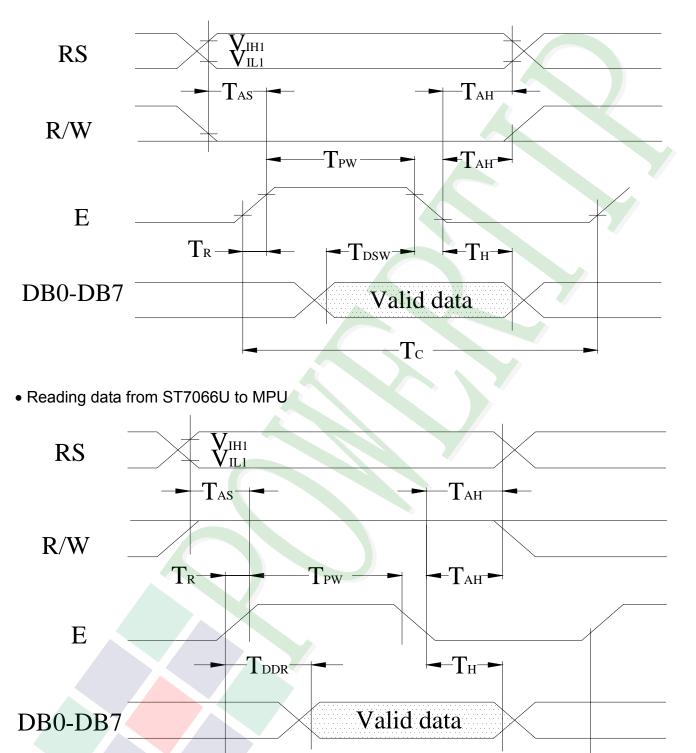
}

{ delay(40); write\_com(0x01); delay(5); write\_com(0x38); delay(5); write\_com(0x0c); delay(5); write\_com(0x06); delay(5);



### 2.3 Timing Characteristics

• Writing data from MPU to ST7066U



-Tc



#### • Write Mode (Writing data from MPU to ST7066U)

				(\	/DD = 5V,	,Ta=25°C)
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
Tc	Enable Cycle Time	Pin E	1200	-	-	ns
T <sub>PW</sub>	Enable Pulse Width	Pin E	140	-	4	ns
$T_R, T_F$	Enable Rise / Fall Time	Pin E	-	-	25	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T <sub>AH</sub>	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T <sub>DSW</sub>	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
Т <sub>н</sub>	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

• Read Mode (Reading data from ST7066U to MPU)

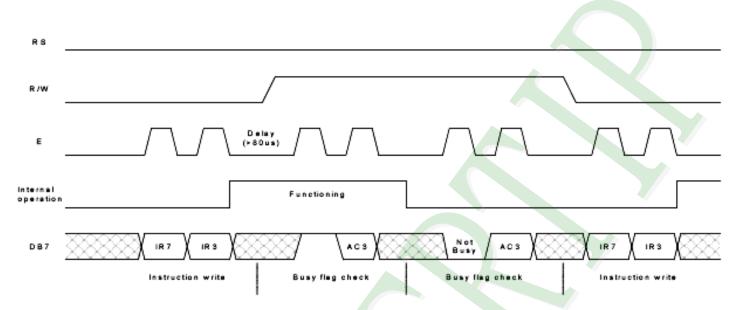
		-			VDD = 5V	/, Ia=25°C
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T <sub>C</sub>	Enable Cycle Time	Pin E	1200	-	-	ns
T <sub>PW</sub>	Enable Pulse Width	Pin E	140	-	-	ns
$T_R, T_F$	Enable Rise / Fall Time	Pin E	-	I	25	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T <sub>AH</sub>	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
$T_{DDR}$	Data Setup Time	Pins:DB0~DB7		-	100	ns
Τ <sub>Η</sub>	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

חר



For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used

Exa	mple of busy flag check timing sequence
RS	
R/W	
E	Delay (>80us)
Internal operation	Functioning
D 67	Date         Busy         Busy         Not Busy         Date           Instruction write         Busy flag check         Busy flag check         Busy flag check         Instruction write



## 2.4 Display Command

		Instruction Code										Description
Instructions	RS	R/	DB	DB	DB	DB	DB	DB	DB	DB	Description	Time
	R9	W	7	6	5	4	3	2	1	0		(270KHz)
Clear											Write "20H" to DDRAM. and set	
Display	0	0	0	0	0	0	0	0	0	1	DDRAM address to "00H" from	1.52ms
Display											AC.	
											Set DDRAM address to "00H"	
Return											from AC and return cursor to it's	
Home	0	0	0	0	0	0	0	0	1	×	original position if shifted.	1.52ms
Tiome									1		The contents of DDRAM	
											are not changed.	
											Sets cursor move direction and	
Entry Mode	0	0	0	0	0	0	0	1	I/D	S	specifies display shift. These	<b>37</b> µs
Set		Ū	Ū	Ū		Ū					operations are performed	• · p·•
											during data write and read .	
Display											D=1 : entire display on	
ON/OFF	0	0	0	0	0	0	1	D	С	В	C=1 : cursor on	<b>37</b> μ <b>s</b>
											B=1 : cursor position on	
Cursor or											Set cursor moving and display	
Display	0	0	0	0	0	1	S/C	R/L	×	×	shift control bit, and the	<b>37</b> μ <b>s</b>
Shift	Ŭ										the direction, without changing	·
											of DDRAM data.	
Function											DL: interface data is 8/4 bits	
Set	0	0	0	0	1	DL	Ν	F	×	×	NL: number of line is 2/1	<b>37</b> μ <b>s</b>
											F: font size is 5×11/5×8	
Set	_				AC	AC	AC	AC	AC	AC	Set CGRAM address	~ -
CGRAM	0	0	0	1	5	4	3	2	1	0	in address counter.	<b>37</b> μ <b>s</b>
Address												
Set		6		AC	AC	AC	AC	AC	AC	AC	Set DDRAM address	07
DDRAM	0	0	1	6	5	4	3	2	1	0	in address counter.	<b>37</b> μ <b>s</b>
Address												
Deed Dur											Whether during internal	
Read Busy			В	AC	AC	AC	AC	AC	AC	AC	operation or not can be	0 -
Flag and	0	1	F	6	5	4	3	2	1	0	known by reading BF.	<b>0</b> μ <b>s</b>
Address											The contents of address	
											counter can also be read.	



Write Data to RAM	1	0	D 7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37µs
Read Data from RAM	1	1	D 7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF Refer to Instruction Table for the list of each instruction execution time .

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## 2.5 Character Pattern

## NO.7066-0A

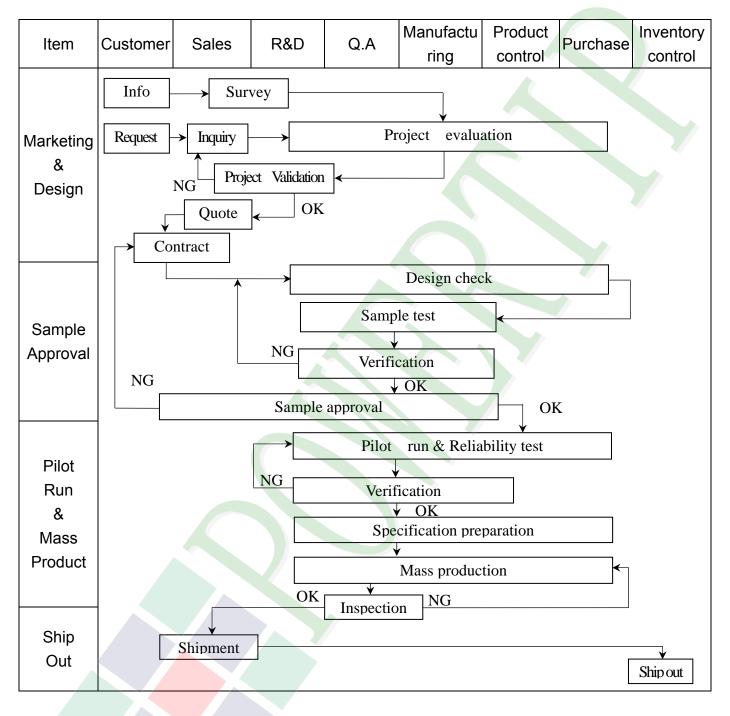
67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															
0001	(2)															
0010	(3)															
0011	(4)															
0100	(5)															
0101	(6)															
0110	0															
D 11 1	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(6)															
1110	Ø															
1111	(8)															

# 2.6 JUMPER (Setting different use)



# **3. QUALITY ASSURANCE SYSTEM**

## 3.1 Quality Assurance Flow Chart





Item	Customer	Sales	R&D	Q.A	Manufact uring	Product control	Purchase	Inventory control
Sales Service	Info Analys	→ Claim -	[	Trackin	Failure an Corrective			
Q.A Activity1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management2. Process improve 4. Education And T						es		

# POWERTIP

#### 3.2 **Inspection Specification**

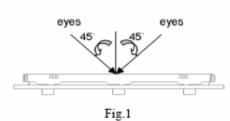
Scope : The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).

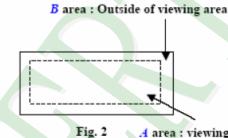
◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level: Major Defect AQL:0.4 ; Minor Defect:AQL: 1.5.
- OUT Going Defect Level : Sampling .

#### Manner of appearance test :

- (1). The test be under 20W×2 fluorescent light ' and distance of view must be at 30 cm.
- (2). Standard of inspection : (Unit : mm)
- (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
- (4). Definition of area . (Fig. 2)





A area : viewing area

#### Specification:

NO	Item	Criterion	Level
		1. 1 The part number is inconsistent with work order of Production.	Major
01	Product condition	1. 2 Mixed production types.	Major
		1.3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major
		4. 1 Missing line character and icon.	Major
		4. 2 No function or no display.	Major
04	Electrical Testing	4. 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4.5 Current consumption exceeds product specifications.	Major



Specification For Monotype and Color STN: (Ver.									
NO	Item	C	Criteri	on			Level		
	Black or white dot 、scratch 、 contamination	<ul> <li>5. 1 Round type:</li> <li>5. 1. 1 display only :</li> <li>• White and black spots on display ≤ 0. 30 mm, no more than 4 white or black spots present.</li> <li>• Densely spaced : NO more than two spots or lines within 3 mm.</li> </ul>							
		5. 1. 2 Non-display :							
	Round type	Dimension (diameter : Φ)	Acceptance (Q'ty) A area B area						
		Φ≤ 0.10		ept no dense	D	area			
	+ x  ++	$0.10 < \Phi \leq 0.20$		3					
05	<b>₩</b>	$0.10 < \Phi \le 0.20$ $0.20 < \Phi \le 0.30$		2	Ignore		Minor		
	Φ=(x+y)/2	Total quantity		4					
	$\Psi^{-}(\mathbf{x} + \mathbf{y})/2$			4					
		5. 1. 3 Line type:							
	Line type	Dimension	Acceptance (Q'ty)						
	Line type	Length (L) Width (W)		A area		B area			
	∽ / <sup>‡</sup> <sup>w</sup>	W≦		Accept no de	nse				
		$L \le 3.0$ $0.03 < W \le$	4						
	2	$L \le 2.5$ 0.05 $< W \le 0$	. 075						
		W >0	. 075 As round type						
		Dimension		Acceptan	ce (Q				
		(diameter : Φ)		A area		B area	<u>i</u>		
		$\Phi \leq 0.20$	A	ccept no dense					
06	Polarizer	$0.20 < \Phi \leq 0.50$	3				Minor		
	Bubble	$0.50 < \Phi \le 1.00$	2			Ignore			
		$\Phi > 1.00$	0						
		Total quantity	4						
			<u> </u>						

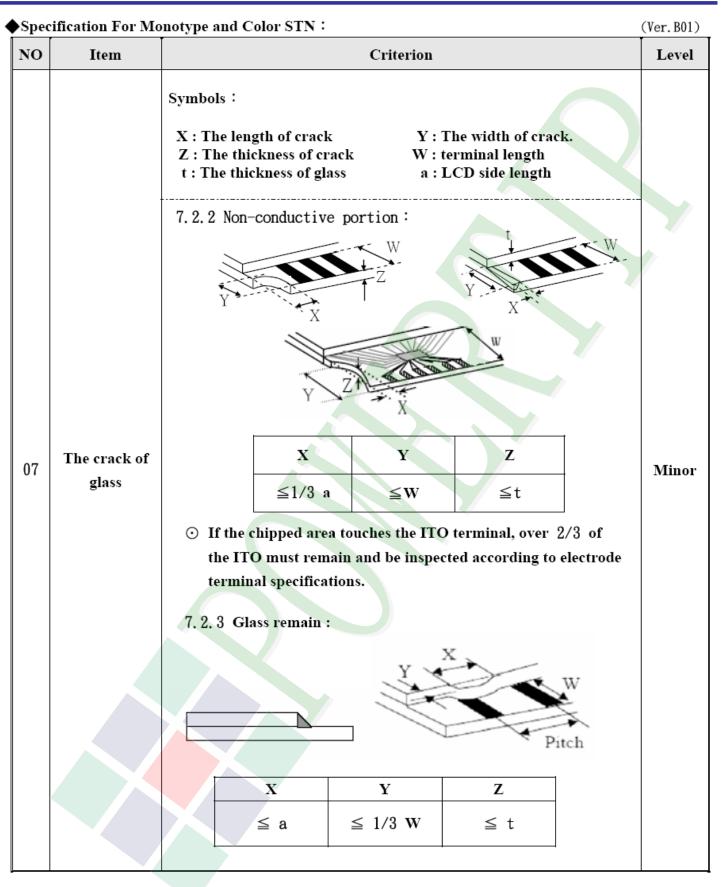


Specification For Monotype and Color STN : (Ver. B01)								
NO	Item	Criterion		Level				
		Z : The thickness of crack W	: The width of crack. : terminal length : LCD side length					
		7.1 General glass chip: 7.1.1 Chip on panel surface and crac	k between panels:					
		Y Z Z	Z Y Y X					
07	The crack of glass		SP [NG]	Minor				
		[OK] Seal width	Y					
		XY	Z					
		≤ a Crack can't enter viewing area	$\leq 1/2 t$					
		≤ a Crack can't exceed the half of SP width.	$\begin{array}{c c} \mathbf{e} & 1/2 \mathbf{t} < \mathbf{Z} & \leq 2 \mathbf{t} \end{array}$					



Specification For Monotype and Color STN : (Ver.)									
NO	Item	Criterion	Level						
		Symbols :X : The length of crackY : The width of crack.Z : The thickness of crackW : terminal lengtht : The thickness of glassa : LCD side length							
		7.1.2 Corner crack :							
		X Y Z							
	The crack of	$\leq 1/5 \text{ a} \qquad \begin{array}{c} \text{Crack can't enter} \\ \text{viewing area} \end{array} \qquad $							
		$\leq 1/5 \text{ a}  \begin{array}{c} \text{Crack can't exceed the} \\ \text{half of SP width.} \end{array}  1/2 \text{ t} < \text{Z}  \leq 2 \text{ t} \end{array}$							
07	glass	7.2 Protrusion over terminal:							
		7.2.1 Chip on electrode pad :							
		X							
		X Y Z							
		Front $\leq$ a $\leq$ 1/2 W $\leq$ t							
		Back Neglect							







Specification For Monotype and Color STN : (Ver. I								
NO	Item	Criterion	Level					
		8. 1 Backlight can't work normally.	Major					
08	Backlight elements	8. 2 Backlight doesn't light or color is wrong.	Major					
		8. 3 Illumination source flickers when lit.	Major					
		9. 1 Pin type must match type in specification sheet.	Major					
		9. 2 No short circuits in components on PCB or FPC.	Major					
09	General appearance	9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor					
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor					
		9.5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≦1.5 mm.	Minor					



# 4. RELIABILITY TEST

4.1 Reliability Test Condition

4.1									
NO.	TEST ITEM	TEST CO	NDITION						
1	High Temperature	<b>Keep in +80 ±2°</b> C 96 hrs							
	Storage Test	Surrounding temperature, then sto	orage at normal condition 4hrs.						
2	Low Temperature	Keep in −30 ±2°C 96 hrs							
	Storage Test	Surrounding temperature, then sto							
	High Temperature /	Keep in +60 $^{\circ}$ / 90% R.H duration							
3	High Humidity	Surrounding temperature, then storage at normal condition 4hrs.							
	Storage Test	(Excluding the polarizer)							
			$\rightarrow +80^{\circ}C \rightarrow +25^{\circ}C$						
4	Temperature Cycling	(30mins) (5mins)	(30mins) (5mins)						
	Storage Test	10 Cycle							
		Surrounding temperature, then sto	orage at normal condition 4hrs.						
		Air Discharge:	Contact Discharge:						
	ESD Test	Apply 2 KV with 5 times	Apply 250 V with 5 times						
		Discharge for each polarity +/-	discharge for each polarity +/-						
		<b>1.</b> Temperature ambiance : $15^{\circ}$ C $\sim$							
5		2. Humidity relative : $30\% \sim 60\%$							
		3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%							
		4. Discharge Resistance(Rd) : 330 Ω±10%							
		5. Discharge, mode of operation : Single Discharge (time between guessessive discharges et least 1 and							
		Single Discharge (time between successive discharges at least 1 sec) (Talaranae if the output voltage indication $\pm 50$ ()							
		(Tolerance if the output voltage indication : ±5%)							
_	Vibration Test	1. Sine wave $10 \sim 55$ Hz frequence							
6	(Packaged)	2. The amplitude of vibration :1. 5 mm							
		3. Each direction $(X \cdot Y \cdot Z)$ due	ration for 2 Hrs						
		Packing Weight (Kg)	Drop Height (cm)						
		0 ~ 45.4	122						
_	Drop Test	45.4 ~ 90.8	76						
7	(Packaged)	90.8 ~ 454	61						
		0ver 454	46						
		Drop Direction : %1 corner / 3 edg	Drop Direction : ※1 corner / 3 edges / 6 sides each 1 time						

(Ver.B01)

# **POWERTIP**

# **5. PRECAUTION RELATING PRODUCT HANDLING**

## 5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

## 5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

## **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}C \pm 5^{\circ}C$  and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

## **5.4 TERMS OF WARRANTY**

5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.

#### 5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

