



Description

- Standard 7 x 5mm crystal oscillator
Ceramic package with a seam sealed metal lid, hermetically sealed
- Model: CFPS-72
- Model Issue number: 12

Frequency Parameters

- Frequency: 50.0MHz
- Frequency Stability: $\pm 50.00\text{ppm}$
- Operating Temperature Range: 0.00 to 70.00°C
- Ageing: $\pm 3\text{ppm}$ per year max

Electrical Parameters

- Supply Voltage: 5.0V $\pm 10\%$
- Current Draw: 50.00mA

Output Details

- Output Compatibility: HCMOS/TTL
- Drive Capability: 50pF max
- Rise and Fall Time: 6ns max
- Duty Cycle: 45/55%

Output Control

- Standby Operation
Logic '1' (>70% VS) to pad 1 enables oscillator output
Logic '0' (<30% VS) to pad 1 disables oscillator output; the oscillator output goes to the high impedance state
No connection to pad 1 enables oscillator output
Standby Current: 10 μ A max

Noise Parameters

- Phase Noise (@125.0MHz) typical:
 - 10Hz -54dBC/Hz
 - 100Hz -93dBC/Hz
 - 1KHz -131dBC/Hz
 - 10KHz -150dBC/Hz
 - 100KHz -160dBC/Hz
 - 1MHz -163dBC/Hz
 - 10MHz -164dBC/Hz
- Phase Jitter RMS (@:125.0MHz): 53fs typical

Environmental Parameters

- Shock: MIL-STD-202, Method 213, Condition E
- Vibration: MIL-STD-883, Method 2007, Condition A

Compliance

- RoHS Status (2011/65/EU): Compliant
- REACH Status: Compliant
- MSL Rating (JDEC-STD-033): Not Applicable

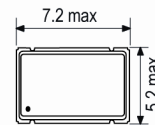
Packaging Details

- Pack Style: Bulk Loose in bulk pack
Pack Size: 100
- Alternative packing option available

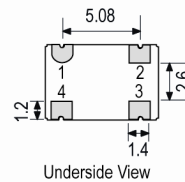
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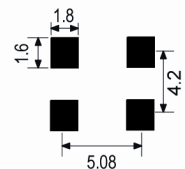
Outline (mm)



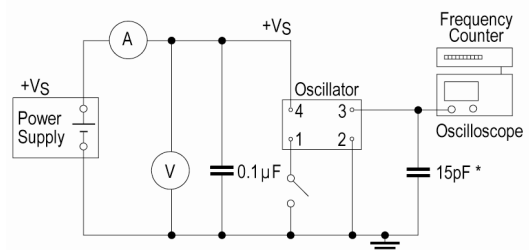
- Pad Connections
- Standby Operation
 - GND
 - Output
 - +VS



Solder Pad Layout



Test Circuit



* Inclusive of jigging and equipment capacitance

Sales Office Contact Details:

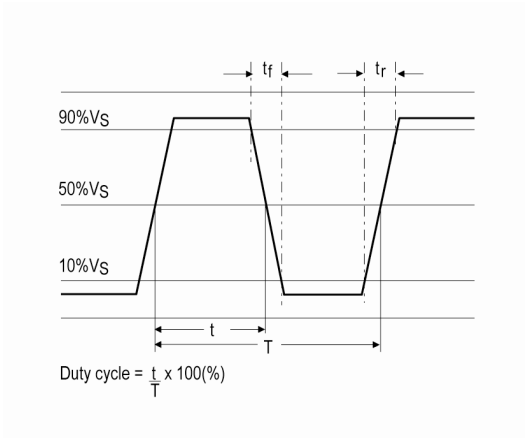
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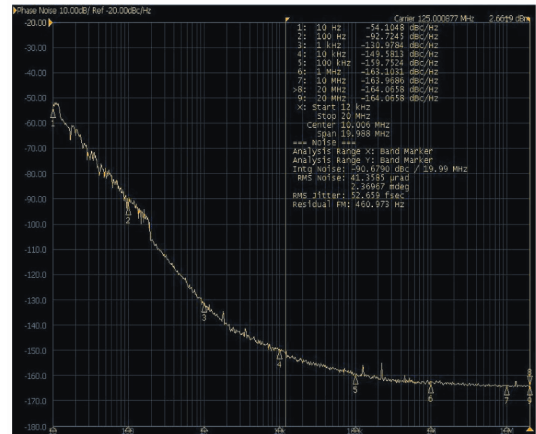
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Wave Form



Phase Noise / Jitter



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