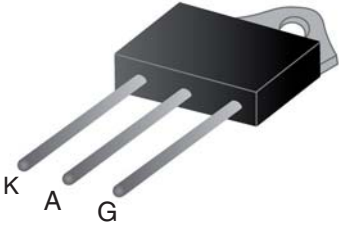
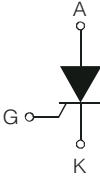



## INSULATED STANDARD 50A SCR

<p><b>INSULATED TO3P</b></p>   	<p><b>On-State Current</b> 50 Amp</p>	<p><b>Gate Trigger Current</b> ≤ 80 mA</p>	
	<p><b>Off-State Voltage</b> 600 V ÷ 1200 V</p>		
	<p><b>FEATURES</b></p> <ul style="list-style-type: none"> <li>• Glass/passivated die junctions</li> <li>• High current SCR</li> <li>• Low thermal resistance</li> <li>• High surge current capability</li> <li>• Low forward voltage drop</li> <li>• Solder dip 260°C, 10s</li> <li>• Component in accordance to RoHS 2011/65/EU and WEEE 2002/96/EC</li> <li>• Meets MSL level 3, per J-STD-020, LF maximum peak of 260° C</li> <li>• Certified compliance of UL 1557 Standard for Electrically Isolated Semiconductors. Fille reference E320541, Vol. 3</li> </ul>		 <p><b>RoHS</b> COMPLIANT</p>
	<p><b>MECHANICAL DATA</b></p> <ul style="list-style-type: none"> <li>• <b>Case:</b> INSULATED TO3P. Epoxy meets UL 94V-0 flammability rating.</li> <li>• <b>Polarity:</b> As marked on the body.</li> <li>• <b>Terminals:</b> Matte tin plated leads, solderable per MIL-STD-750 Method 2026, J-STD-002 and JESD22-B102. Consumer grade, meets JESD 201 class 1A whisker test.</li> </ul>		
<p><b>TYPICAL APPLICATIONS</b></p> <p>Thanks to its triggering levels, the FS50xxxP SCR series is suitable to fit all modes of control, found in applications such as overvoltage crowbar protection, motor control circuits in power tools and kitchen aids, inrush current limiting circuits, capacitive discharge ignition and voltage regulation circuits.</p>			

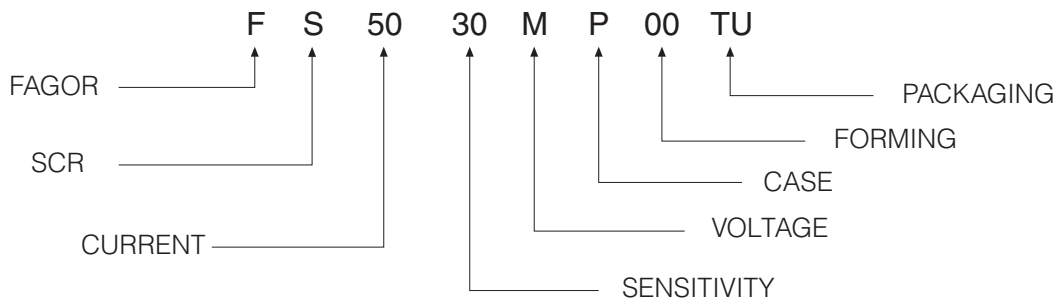
### Maximun Ratings and Electrical Characteristics at 25°C

SYMBOL	PARAMETER	CONDITIONS	Value	Unit
$I_{T(RMS)}$	RMS On-state Current (full sine wave)	All Conduction Angle, $T_c = 75^\circ C$	50	A
$I_{TSM}$	Non-repetitive On-State Current	Full Cycle, 60 Hz ( $t_p = 8.3$ ms)	610	A
$I_{TSM}$	Non-repetitive On-State Current	Full Cycle, 50 Hz ( $t_p = 10$ ms)	580	A
$I^2t$	Fusing Current	$t_p = 10$ ms, Half Cycle	1680	A <sup>2</sup> s
$I_{GM}$	Peak Gate Current	20 $\mu$ s max. $T_j = 125^\circ C$	8	A
$P_{G(AV)}$	Average Gate Power Dissipation	$T_j = 125^\circ C$	1	W
$di/dt$	Critical rate of rise of on-state current	$I_G = 2 \times I_{GT}$ , $t_r \leq 100$ ns $f = 60$ Hz, $T_j = 125^\circ C$	50	A/ $\mu$ s
$T_j$	Operating Temperature		(-40 +125)	°C
$T_{stg}$	Storage Temperature		(-40 +150)	°C

SYMBOL	PARAMETER	VOLTAGE			Unit
		M	N	Q	
$V_{DRM}/V_{RRM}$	Repetitive Peak Off State Voltage	600	800	1200	V

**INSULATED STANDARD 50A SCR**
**Electrical Characteristics at Tamb = 25 °C**

SYMBOL	PARAMETER	CONDITIONS		SENSITIVITY	Unit
				30	
I <sub>GT</sub>	Gate Trigger Current	V <sub>D</sub> = 12 V <sub>DC</sub> , R <sub>L</sub> = 33Ω, T <sub>j</sub> = 25 °C	MAX	80	mA
V <sub>GT</sub>	Gate Trigger Voltage	V <sub>D</sub> = 12 V <sub>DC</sub> , R <sub>L</sub> = 33Ω, T <sub>j</sub> = 25 °C	MAX	1.3	V
V <sub>GD</sub>	Gate Non Trigger Voltage	V <sub>D</sub> = V <sub>DRM</sub> , R <sub>L</sub> = 3.3 KΩ, T <sub>j</sub> = 125 °C	MIN	0.2	V
I <sub>H</sub>	Holding Current	I <sub>T</sub> = 500 mA, Gate open,	MAX	150	mA
I <sub>L</sub>	Latching Current	I <sub>G</sub> = 1.2 x I <sub>GT</sub> , T <sub>j</sub> = 25 °C	MAX	200	mA
dV/dt	Critical Rate of Voltage Rise	V <sub>D</sub> = 0.67 x V <sub>DRM</sub> , Gate open T <sub>j</sub> = 125 °C	MIN	1000	V/μs
V <sub>TM</sub>	On-state Voltage	I <sub>TM</sub> = 100 Amp, tp = 380 μs, T <sub>j</sub> = 25 °C	MAX	1.9	V
V <sub>t(o)</sub>	Threshold Voltage	T <sub>j</sub> = 125 °C	MAX	1	V
r <sub>d</sub>	Dynamic resistance	T <sub>j</sub> = 125 °C	MAX	8.5	mΩ
I <sub>DRM</sub> /I <sub>RRM</sub>	Off-State Leakage Current	V <sub>DRM</sub> =V <sub>RRM</sub> T <sub>j</sub> = 125 °C	MAX	5	mA
		T <sub>j</sub> = 25 °C	MAX	10	μA
R <sub>th(j-c)</sub>	Thermal Resistance Junction-Case D.C.	D.C.		0.9	°C/W

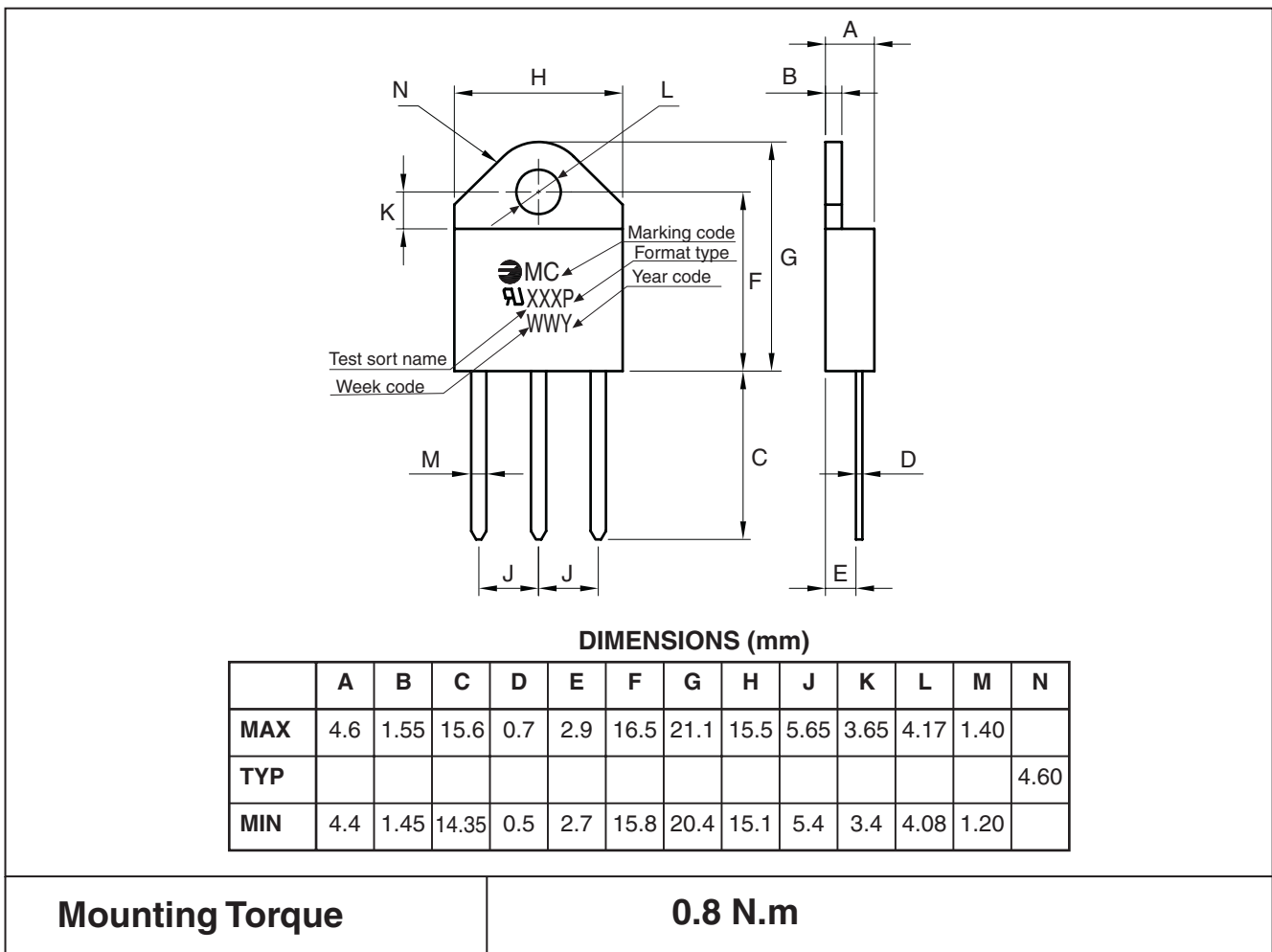
**Part Number Information**


**INSULATED STANDARD 50A SCR**

**Ordering information**

PREFERRED P/N	PACKAGE CODE	DELIVERY MODE	BASE QUANTITY	UNIT WEIGHT (g)
FS5030MP 00TU	TU	TUBE	450	4.50

**Package Outline Dimensions: (mm) INSULATED TO3P**



**INSULATED STANDARD 50A SCR**

**Ratings and Characteristics (Ta 25 °C unless otherwise noted)**

Fig. 1: Maximum power dissipation versus average on-state current

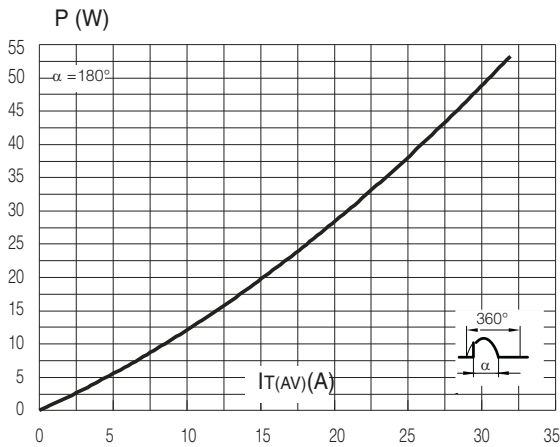


Fig. 2: Average and D.C. on-state current versus case temperature

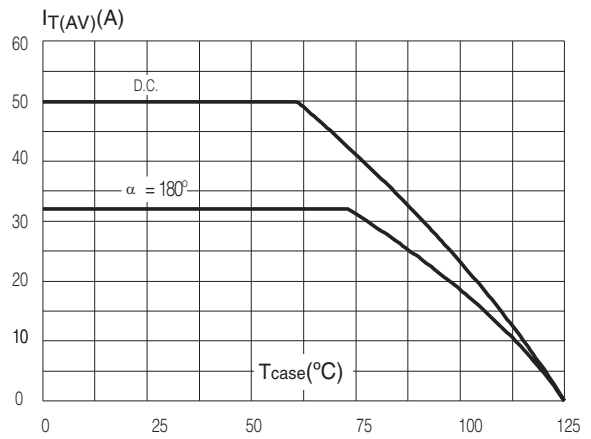


Fig. 3: Relative variation of thermal impedance versus pulse duration

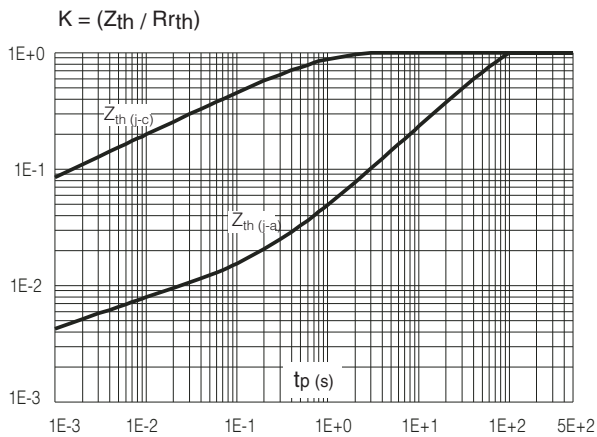
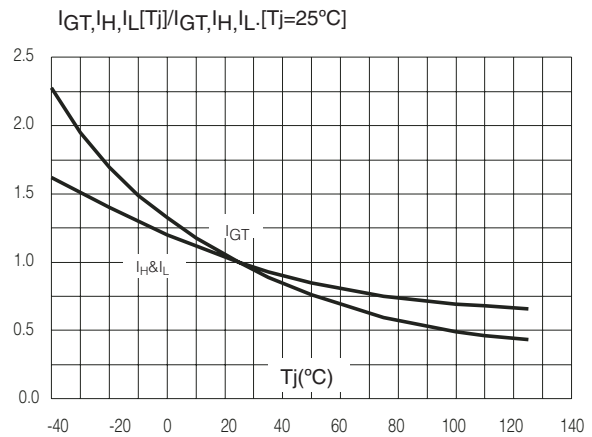


Fig. 4: Relative variation of gate trigger current, holding current and latching current versus junction temperature



**INSULATED STANDARD 50A SCR**

Fig. 5: Surge peak on-state current versus number of cycles

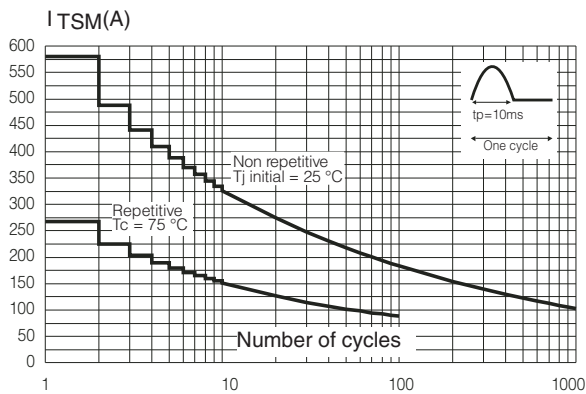


Fig. 6: Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10$  ms, and corresponding values of  $I^2t$

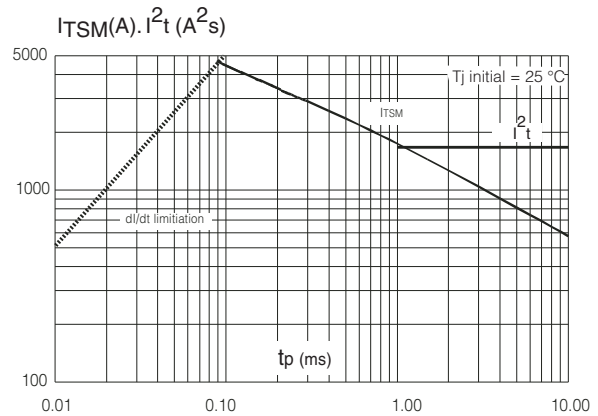
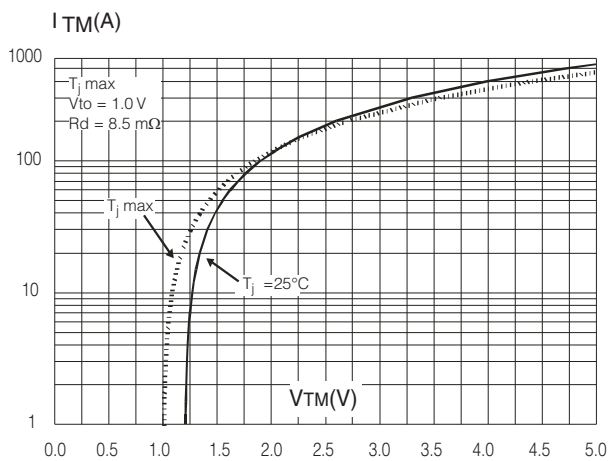


Fig. 7: On-state characteristics (maximum values)



**INSULATED STANDARD 50A SCR**

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