

**Display Elektronik GmbH**

# DATA SHEET

**TFT MODULE**

**DEM 1024600E TMH-PW-N**

**7,0“ TFT**

*Product Specification*

*Ver.: 1*

**16.02.2017**

Revise Records

Rev.	Date	Contents	Written	Approved
0	24.10.2016	Preliminary Specification	MH	MH
1	16.02.2017	1. Revise the Outline Dimension 2. Modify the Voltage of Backlight	MH	MH

Special Notes

Note1.	
Note2.	
Note3.	
Note4.	
Note5.	

## Contents

1. General Description and Features.....	4
1.1. Features	4
1.2. LCD Module	4
2. Mechanical Information.....	4
3. Absolute Max. Ratings.....	5
3.1. Absolute Ratings of Environment	5
3.2. Absolute Ratings of Electrical	5
4. Electrical Characteristics.....	6
4.1. TFT-LCD Module	6
4.2. Backlight Unit	12
5. Input Terminal Pin Assignment.....	13
5.1. Pin Assignment (LCD)	13
5.2. Pin Assignment (Backlight unit)	14
6. Optical Characteristics.....	15
7. Reliability Condition .....	17
8. DIMANSIONAL OUTLINE.....	18
9. Incoming Inspection Standards.....	19
9.1. INSPECTION and Environment Conditions	19

**1. General Description and Features**

This TFT is a 7.0" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, driver ICs, control circuit and LED backlight. By applying 1024×600 images are displayed on the 7.0" diagonal screen. Display 16.7M colors by R.G.B signal input.

**1.1. Features**

- 1024 x RGB x 600 Pixels Resolution.
- Display in 16.7 Million colors.
- RoHS Compliance

**1.2. LCD Module**

Item	Specification	Unit
Screen Size	7.0 Inches	Diagonal
Display Resolution	1024 x RGB x 600	Dot
Active Area	154.2144 x 85.92	mm
Outline Dimension	170.00 x 110.00 x 6.75	mm
Display Mode	Normally White	--
Pixel Arrangement	RGB-Vertical Stripe	--
Pixel Size	0.1506 × 0.1432	mm
Surface Treatment	Anti-Glare	--
Display Color	16.7 Million	--
Viewing Direction	6 o'clock (Gray Inversion)	--
Input Interface	LVDS	--

**2. Mechanical Information**

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	169.70	170.00	170.30	mm	--
	Vertical (V)	109.70	110.00	110.30	mm	
	Thickness (T)	6.45	6.75	7.05	mm	(1)
Weight		--	TBD	--	g	--

Note (1) Not include Component.

Refer to the Outline Dimension for further information.

### 3. Absolute Max. Ratings

#### 3.1. Absolute Ratings of Environment

If the operating condition exceeds the following absolute maximum ratings, the TFT LCD module may be damaged permanently.

(Ta=25±2°C, V<sub>SS</sub>=GND=0)

Item	Symbol	Min.	Max.	Unit	Note
Storage Temperature	T <sub>STG</sub>	-30	+80	°C	(1)
Operating Temperature	T <sub>OPR</sub>	-20	+70	°C	(1,2,3)

Note (1) 95 % RH Max. (40 °C ≥ Ta). Maximum wet-bulb temperature at 39°C or less. (Ta > 40°C)  
No condensation.

Note (2) In case of below 0°C, the response time of liquid crystal (LC) becomes slower and the color of panel becomes darker than normal one. Level of retardation depends on temperature, because of LC's character

Note (3) Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at +25°C.

#### 3.2. Absolute Ratings of Electrical

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

(V<sub>SS</sub>=GND=0)

Item	Symbol	Min.	Max.	Unit	Note
Digital Supply Voltage	DVDD_LVDS	-0.3	3.96	V	
Analog Supply Voltage	AVDD	-0.5	14.85	V	
Gate On Voltage	VGH	-0.3	40	V	
Gate Off Voltage	VGL	-20	0.3	V	
Gate On-Gate Off Voltage	VGH-VGL	12	40	V	
Signal Input Voltage	NIND0 ~ NIND3 PIND0 ~ PIND3 NINC,PINC	-0.5	5	V	
Forward Current (Per LED)	I <sub>f</sub>	-	70	mA	
Reverse Voltage (Per LED)	V <sub>R</sub>	-	5	V	

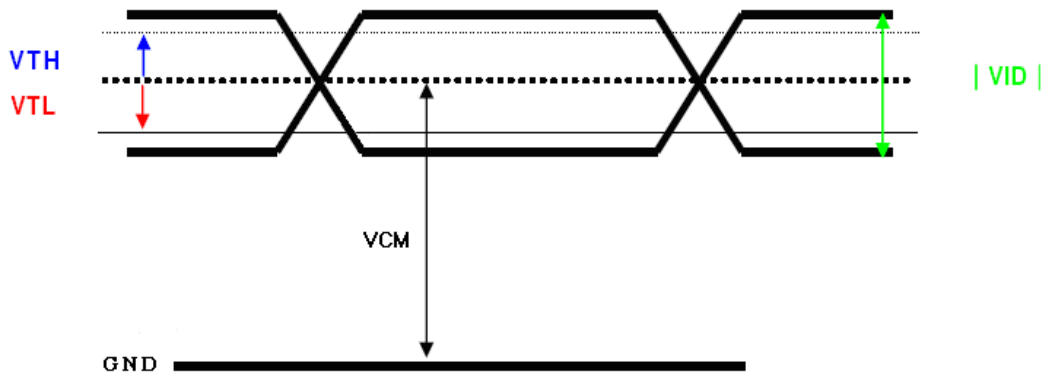
4. Electrical Characteristics

4.1. TFT-LCD Module

4.1.1. Typical Operation Conditions

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Digital Power Supply Voltage For LCD	DVDD	3	3.3	3.6	V	
Logic Input Voltage (LVDS:IN+,IN-)	VCM	$\frac{ VID }{2}$	-	$2.4 - \frac{ VID }{2}$	V	Note1
	VID	200	-	600	mV	Note1
	VTH	-	-	100	mV	VCM=1.2V Note1
	VTL	-100	-	-	mV	
Analog Power Supply Voltage	AVDD	9.4	9.6	9.8	V	
Gate On Power Supply Voltage	VGH	17	18	19	V	
Gate Off Power Supply Voltage	VGL	-6.6	-6	-5.4	V	
Common Power Supply Voltage	VCOM	3.7	3.9	4.1	V	Note2
Logic Input Voltage	VIH	0.7*DVDD	-	DVDD	V	
	VIL	GND	-	0.3*DVDD	V	

【Note1】 LVDS signal



【Note2】 Please adjust VCOM to make the flicker level be minimum.

4.1.2. Current Consumption

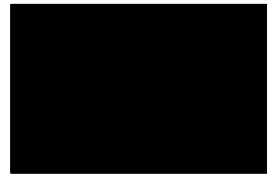
ITEM	SYMBOL	CONDITION	MIN	TYPE	MAX	UNIT	NOTE
Gate On Power Current	IVGH	VGH =18V	--	0.5	1	mA	Note1
Gate Off Power Current	IVGL	VGL= -6V	--	0.5	1	mA	Note1
Digital Power Current	IDVDD	DVDD = 3.3V	--	30	45	mA	Note1
Analog Power Current	IAVDD	AVDD = 9.6V	--	35	45	mA	Note1
Total Power Consumption	PC		--	447	604	mW	Note1

【Note1】 Typ. specification : Gray-level test Pattern

Max. specification : Black test Pattern



256 gray pattern

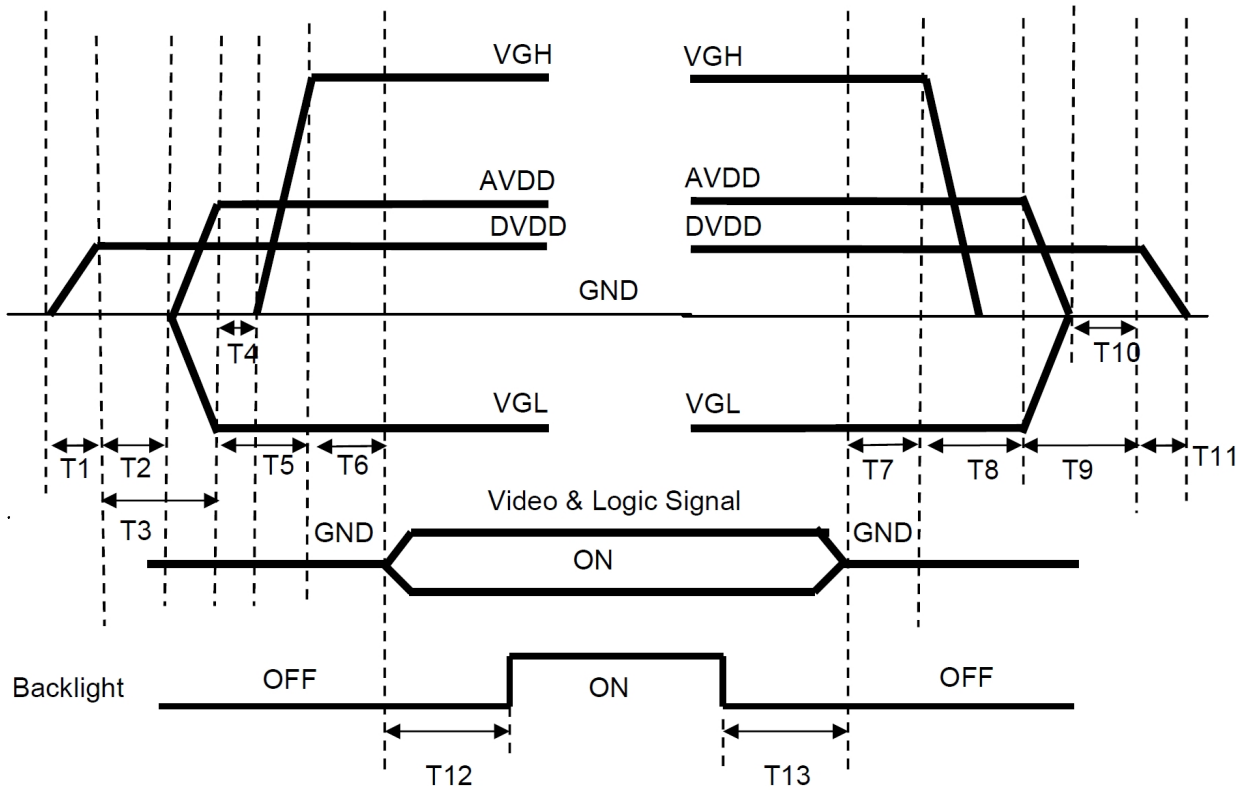


Black Pattern

4.1.3. Power - Signal Sequence

Power On : DVDD→AVDD/VGL →VGH →Video &Logic Signal→Backlight

Power Off : Backlight→Video &Logic Signal→ VGH→AVDD/VGL→DVDD



$0 < T1 \leq 10\text{ms}$   
 $T2 > 0\text{ms}$   
 $T3 > 20\text{ms}$   
 $T4 > 0\text{ms}$   
 $T5 > 10\text{ms}$   
 $0 < T6 \leq 10\text{ms}$   
 $T12 \geq 200\text{ms}$

$T7 > 0\text{ms}$   
 $T8 > 0\text{ms}$   
 $T9 > 0\text{ms}$   
 $T10 > 0\text{ms}$   
 $0 < T11 \leq 10\text{ms}$   
 $T13 \geq 200\text{ms}$

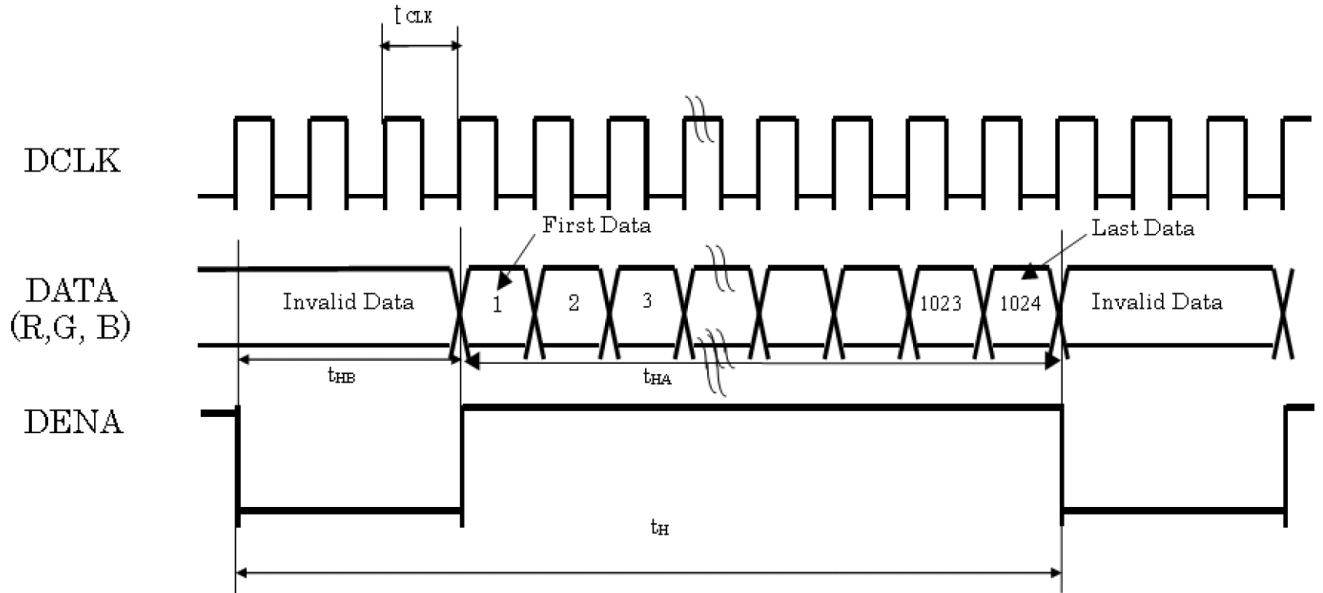


4.1.4. Timing Characteristics of Input Signals

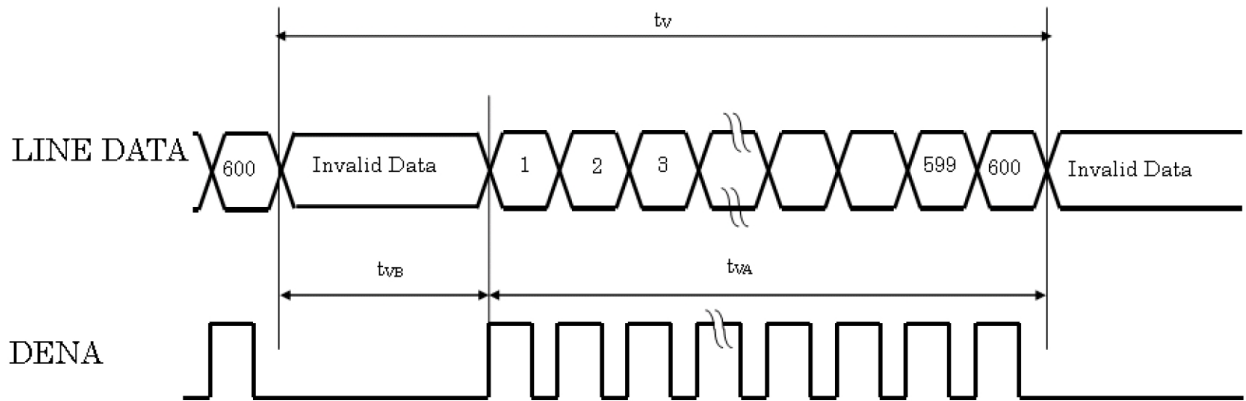
ITEM			SYMBOL	MIN	TYP	MAX	UNIT	
LVDS Input Signal Sequence	CLK Frequency		tclk	45	51.2	57	MHz	
LCD Input Signal Sequence (Input LVDS Transmitter)	DENA	Horizontal	Horizontal total Time	tH	1324	1344	1364	tCLK
			Horizontal effective Time	tHA	1024			tCLK
			Horizontal Blank Time	tHB	300	320	340	tCLK
	Vertical	Vertical	Vertical total Time	tV	625	635	645	tH
			Vertical effective Time	tVA	600			tH
			Vertical Blank Time	tVB	25	35	45	tH

4.1.5. Timing Sequence (Timing Chart)

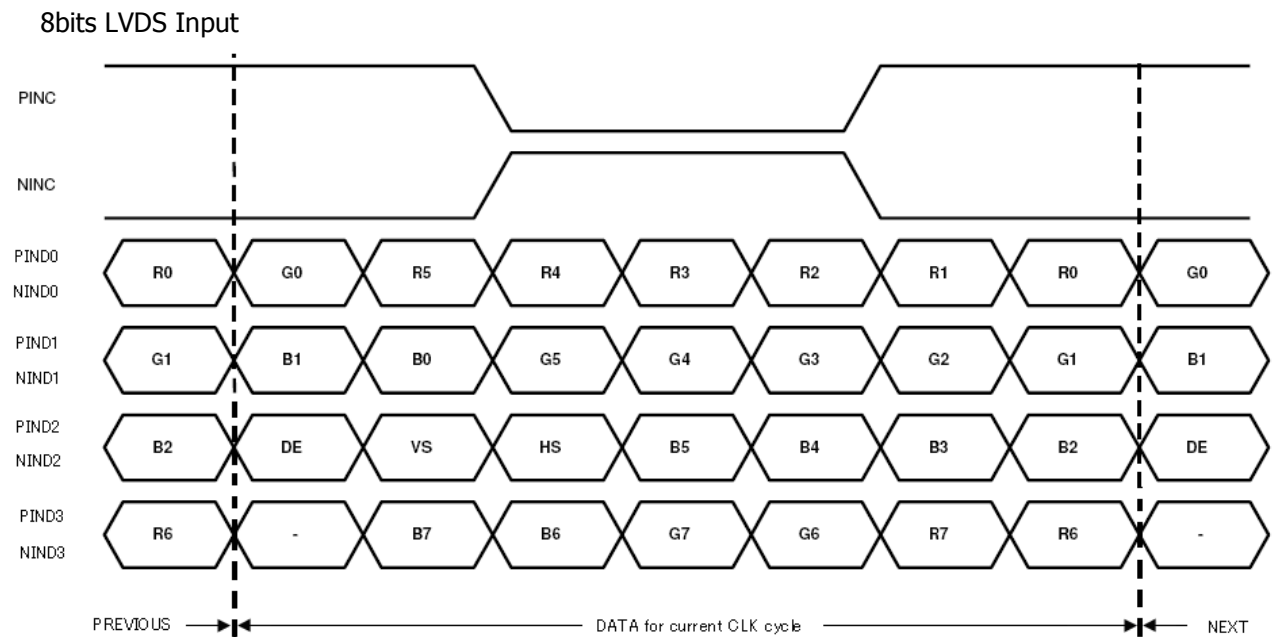
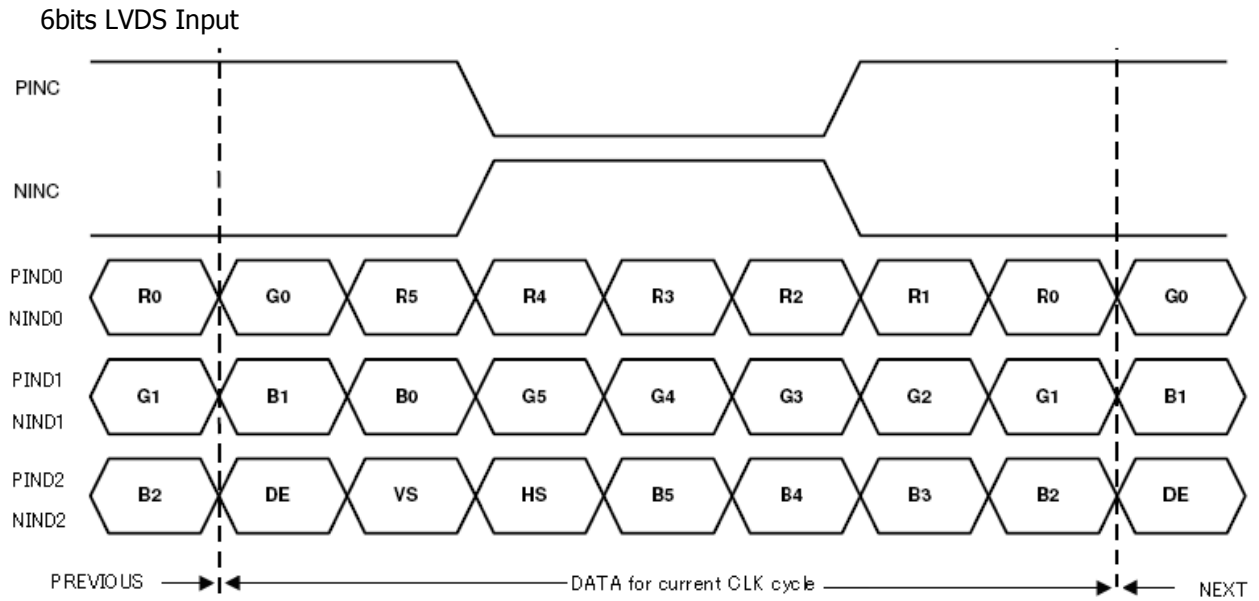
Horizontal Timing Sequence



Vertical Timing Sequence



4.1.6. LVDS Input Data Mapping



**4.2. Backlight Unit**

(Ta=25±2°C)

Item	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
LED Total Input Voltage	VL	8.4	9.0	10.2	V	
LED Total Input Current	IL	-	630	-	mA	(1)
Power Consumption	P <sub>BL</sub>	-	5.67	6.43	W	(2)
Life time	-	50,000	-	-	Hrs	(3)

Note (1) LEDs in 3 series x 14 parallel type.

(2) Where  $IL = 630\text{mA}$ ,  $VL = 9$ ,  $P_{BL} = VB \times IB$

(3) The environmental conducted under ambient air flow, at  $Ta=25\pm 2^\circ\text{C}$ ,  $60\%RH\pm 5\%$

## 5. Input Terminal Pin Assignment

### 5.1. Pin Assignment (LCD)

Pin No.	Symbol	Function	Remark
1	VCOM	Common Voltage	
2	DVDD	Digital Power	
3	DVDD	Digital Power	
4	NC	Not Connect	
5	RESET	Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high. (R=10K $\Omega$ , C=0.1 $\mu$ F)	
6	STBYB	Standby mode, normally pull high STBYB="1", normal operation STBYB="0", timing control, source driver will turn off, all output are high-Z	
7	GND	Ground	
8	NIND0	Negative LVDS differential data input	
9	PIND0	Positive LVDS differential data input	
10	GND	Ground	
11	NIND1	Negative LVDS differential data input	
12	PIND1	Positive LVDS differential data input	
13	GND	Ground	
14	NIND2	Negative LVDS differential data input	
15	PIND2	Positive LVDS differential data input	
16	GND	Ground	
17	NINC	Negative LVDS differential clock input	
18	PINC	Positive LVDS differential clock input	
19	GND	Ground	
20	NIND3	Negative LVDS differential data input	
21	PIND3	Positive LVDS differential data input	
22	GND	Ground	
23	NC	Not Connect	
24	NC	Not Connect	
25	GND	Ground	
26	NC	Not Connect	
27	NC	Not Connect	
28	SELB	6bit/8bit Mode Select	*1)
29	AVDD	Power for Analog Circuit	
30	GND	Ground	
31	NC	Not Connect	

32	NC	Not Connect	
33	SHLR	Horizontal Inversion	*2)
34	UPDN	Vertical Inversion	*2)
35	VGL	Negative Power for TFT	
36	NC	Not Connect	
37	NC	Not Connect	
38	VGH	Positive Power for TFT	
39	NC	Not Connect	
40	NC	Not Connect	

Remarks :

\*1)if LVDS input data is 6bits , SELB must must be set to High

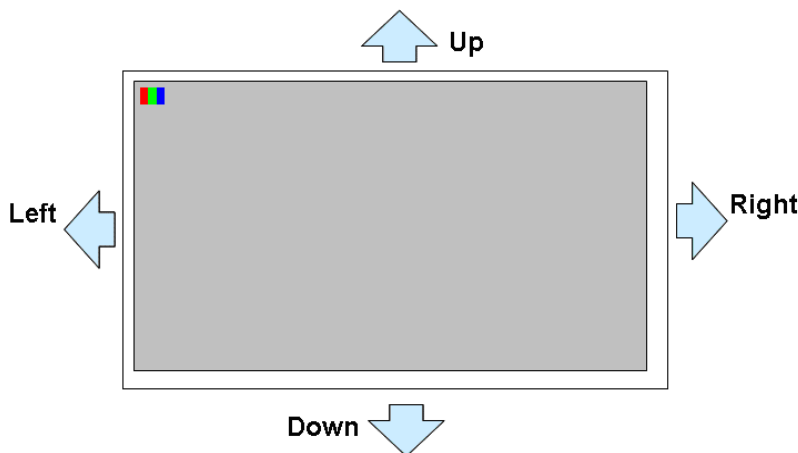
if LVDS input data is 8bit , SELB must be set to Low

\*2)UPDN and SHLR control function

UPDN	SHLR	FUNCTION
0	1	Normal Display
0	0	Inverse Left and Right
1	1	Inverse Up and Down
1	0	Inverse Left and Right Inverse Up and Down

SHLR	UPDN	Data shifting
DVDD	GND	Left→Right , Up→Down(default)
GND	GND	Right→Left , Up→Down
DVDD	DVDD	Left→Right , Down→Up
GND	DVDD	Right→Left , Down→Up

Definition of scanning direction.



**5.2. Pin Assignment (Backlight unit)**

Pin No.	Symbol	Function	Remark
1	A	Power for LED backlight anode	Red
2	K	Power for LED backlight cathode	Black

6. Optical Characteristics

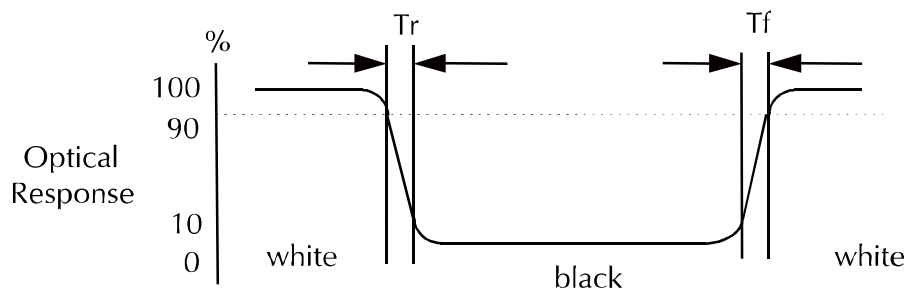
Item	Symbol	Condition	Min	Type	Max	Unit	Note	
Brightness	B		1350	1500	--	cd/m <sup>2</sup>		
Response Time (White - Black)	Tr +Tf	$\theta=0^\circ$	--	25	40	ms		
Contrast Ratio	CR	At optimized viewing angle	600	800	--	--		
Luminance Uniformity	$\Delta L$		75	80		%		
Color Chromaticity (CIE 1931)	White	Wx	$\theta=0^\circ$ Normal Viewing Angle	0.273	0.313	0.353	--	BM-7A
		Wy		0.289	0.329	0.369		
Viewing Angle (6H)	Ver.	$\theta_U$	CR $\geq$ 10	60	70	--	Degree	
		$\theta_D$		50	60	--		
	Hor.	$\theta_L$		70	80	--		
		$\theta_R$		70	80	--		

a. Test equipment setup

After stabilizing and leaving the panel alone shall be warmed up for the stable operation of LCM, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7(fast) with a viewing angle of 2° at a distance of 50cm and normal direction.

b. Definition of response time: Tr and Tf

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

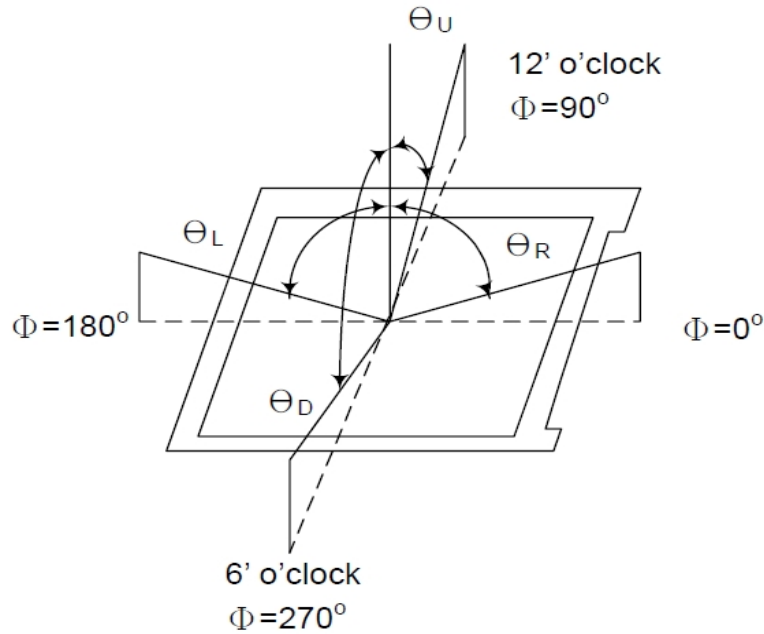


c. Definition of contrast ratio:

$$\text{Contrast Ratio (CR)} = \frac{\text{Brightness measured when LCD is at "white state"}}{\text{Brightness measured when LCD is at "black state"}}$$

d. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

e. View Angle



f. Definition of Luminance of White: Luminance of white at the center points

Light Source of Back-Light Unit	LED Type
---------------------------------	----------

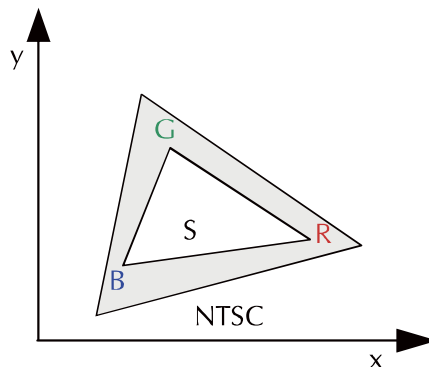
g. Definition of White Uniformity

$$\text{White Uniformity} = \frac{\text{Min. luminance of white among 9-points}}{\text{Max. luminance of white among 9-points}} \times 100\%$$

h. The definition of Color Gamut -Color Chromaticity CIE 1931

Color coordinate of white & red, green, blue at center point.

Color Gamut : NTSC(%) = ( RGB Triangle Area / NTSC Triangle Area ) x 100





**7. Reliability Condition**

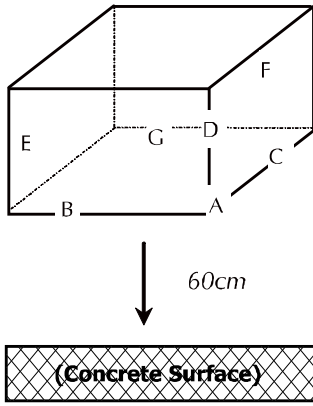
No change on display and in operation under the following test condition.

Condition: Unless otherwise specified, tests will be conducted under the following condition.

Temperature: 20±5°C.

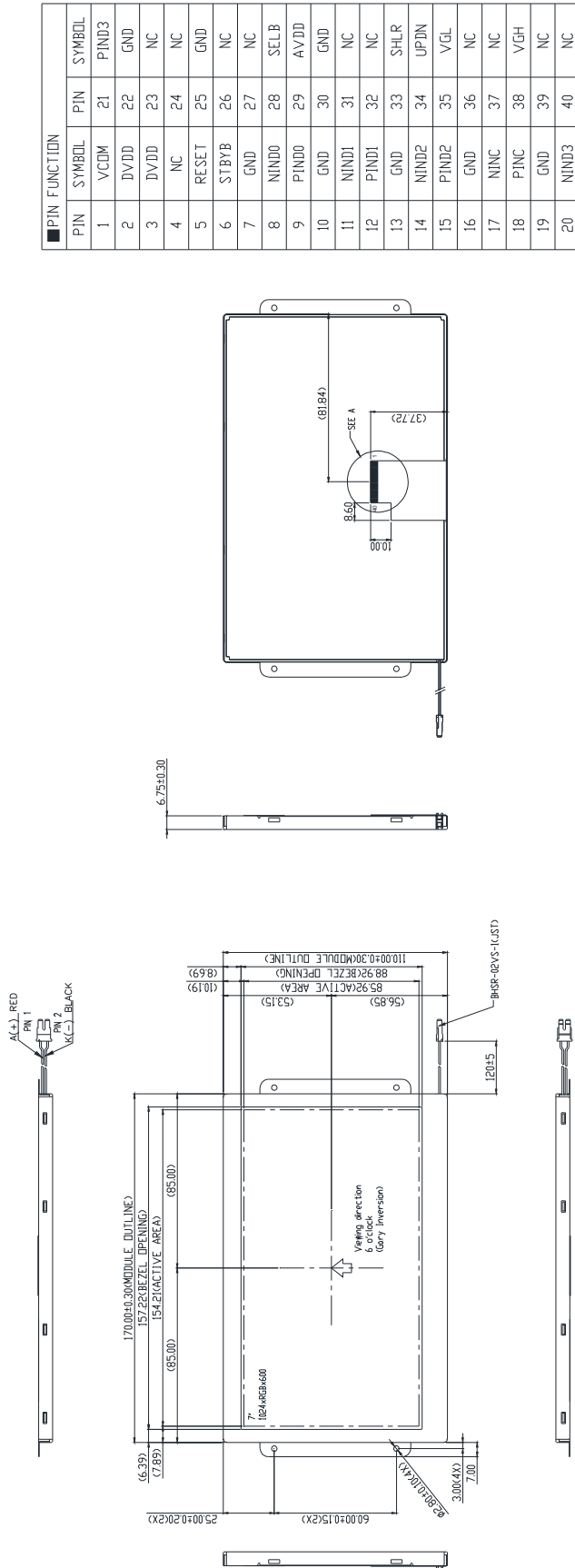
Humidity: 65±5%RH.

Tests will be not conducted under functioning state.

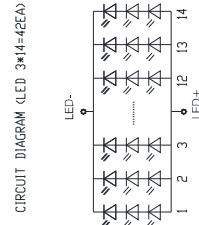
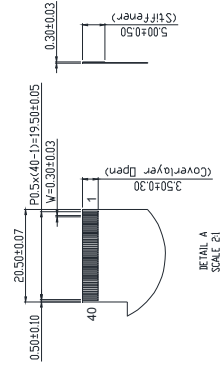
No.	Parameter	Condition	Notes
1	High Temperature Operating	70°C±2°C, 240hrs (Operation state).	
2	Low Temperature Operating	-20°C±2°C, 240hrs (Operation state).	1
3	High Temperature Storage	80°C±2°C, 240hrs.	2
4	Low Temperature Storage	-30°C±2°C, 240hrs.	1,2
5	High Temperature and High Humidity Operation Test	60°C±2°C, 90%, 240hrs.	1,2
6	Vibration Test	Total fixed amplitude: 1.5mm. Vibration Frequency: 10~55Hz. One cycle 60 seconds to 3 direction of X, Y, Z each 15 minutes.	3
7.	Drop Test	To be measured after dropping from 60cm high on the concrete surface in packing state.  <i>Dropping method corner dropping:</i> <i>A corner: Once edge dropping.</i> <i>B, C, D edge: Once face dropping.</i> <i>E, F, G face: Once.</i>	

- Notes:
1. No dew condensation to be observed.
  2. The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.
  3. Vibration test will be conducted to the product itself without putting I in a container.

8. DIMENSIONAL OUTLINE



PIN FUNCTION		
PIN	SYMBOL	PIN SYMBOL
1	VDDM	21 PIND3
2	DVDD	22 GND
3	DVDD	23 NC
4	NC	24 NC
5	RESET	25 GND
6	STBYB	26 NC
7	GND	27 NC
8	NIND0	28 SELB
9	PIND0	29 AVDD
10	GND	30 GND
11	NIND1	31 NC
12	PIND1	32 NC
13	GND	33 SHLR
14	NIND2	34 UPIN
15	PIND2	35 VGL
16	GND	36 NC
17	NINC	37 NC
18	PINC	38 VGH
19	GND	39 NC
20	NIND3	40 NC



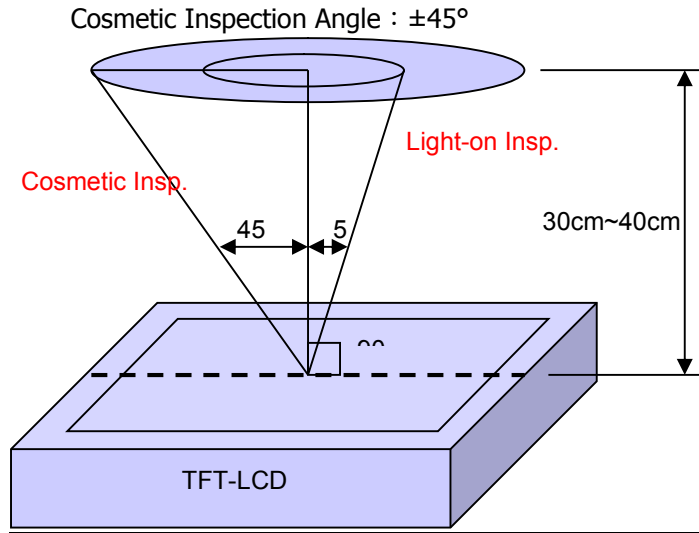
Specification	
Display type	7" TFT-TN
Resolution	1024xRGBx600
Display mode	Normally White
Viewing Direction	6 o'clock (Gray inversion)
Viewing Angle(U/D/L/R)	70/60/80/80
Brightness(Center point)	1500cd/m² Typ.
Color Chromaticity	x=0.313±0.05, y=0.329±0.05
NTSC	50%
Backlight	LED 42pcs, 3S14P 630mA
Operating Temperature	-20~+70°C
Storage Temperature	-30~+80°C
Interface	LVDS

9. Incoming Inspection Standards

9.1. Inspection and Environment Conditions

9.1.1. Inspection Conditions:

- (1) Inspection Distance: 35 cm±5cm
- (2) View Angle : Light-on Inspection Angle : ±5°



( perpendicular to LCD panel surface)

9.1.2. Environment Conditions:

Ambient Temperature		23°C ±5°C
Ambient Humidity		55±10%RH
Ambient Illumination	Cosmetic Inspection	more than 600 Lux
	Functional Inspection	300~500 Lux

9.1.3. Sampling Conditions:

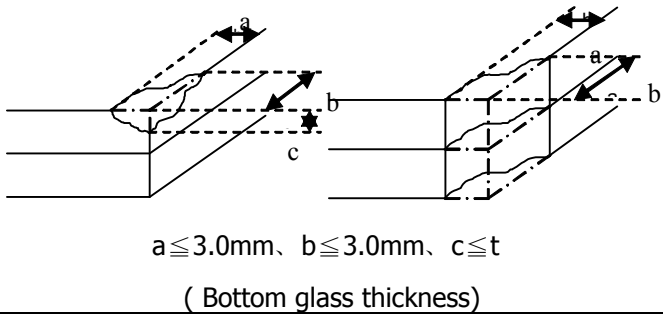
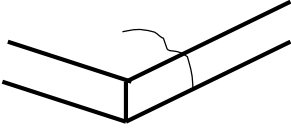
- (1) Lot Size: Quantity of shipment lot per model
- (2) Sampling Method:

Sampling Plan		MIL-STD-105E
		Normal Inspection, Single Sampling
		Level II
AQL	Major Defect	1.0%
	Minor Defect	1.5%

- (3) The classification of Major(MA) and Minor(MI) defects is shown as 3. Inspection Criteria.

9.1.4. Inspection Criteria

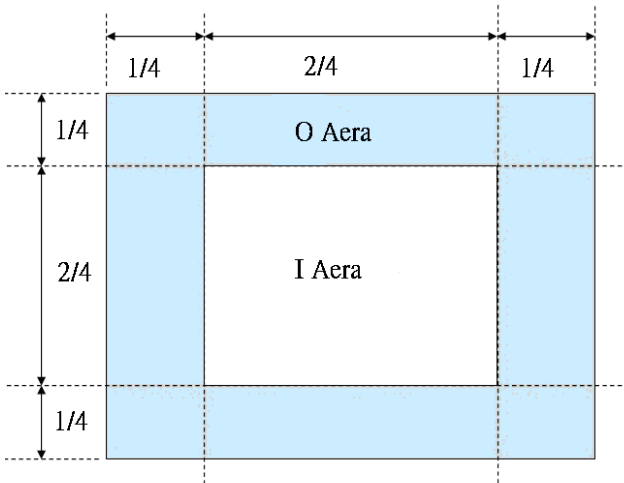
9.1.4.1. Cosmetic Inspection(Panel):

Item	Judgment Criteria	Classification
Chipping on Panel	 <p><math>a \leq 3.0\text{mm}</math>, <math>b \leq 3.0\text{mm}</math>, <math>c \leq t</math> ( Bottom glass thickness)</p>	MA
Scratch on Panel *Note-2	<p><math>W \leq 0.05\text{mm}</math> or <math>L &lt; 5\text{mm}</math>: Ignored  <math>0.05\text{mm} &lt; W \leq 0.1\text{mm}</math> and <math>L \leq 5\text{mm}</math>: <math>N \leq 5</math>  <math>W &gt; 0.1\text{mm}</math> or <math>L &gt; 5\text{mm}</math>: Not allowed</p>	MI
Bubble or Dent on Panel *Note-3	<p><math>D \leq 0.2\text{mm}</math>: Ignored  <math>0.2\text{mm} &lt; D \leq 0.3\text{mm}</math>: <math>N \leq 5</math>  <math>D &gt; 0.3\text{mm}</math>: Not allowed</p>	MI
Panel Crack	 <p>Not Allowed crack</p>	MA
Bezel Deformation	Obvious deformation is not allowed.	MI
Bezel Oxidation	Not allowed if it rusts continuously over 1 cm (It is out of warranty with rusted tin plate)	MI
Bezel Scratch	$L \leq 20\text{mm}$ , $W \leq 0.2$ , $N \leq 3$	MI
Metal Squash Dent /Flange(Front Side)	$D(W) \leq 1, L \leq 3, N \leq 3$ ;	MI
B/L High Voltage Wire Denudation	Not allowed	MA
Polarizer flaw or leak out resin	Defect is defined as the active area.	MI
Outline Dimension	Must in Spec, refer to related product spec.	MI

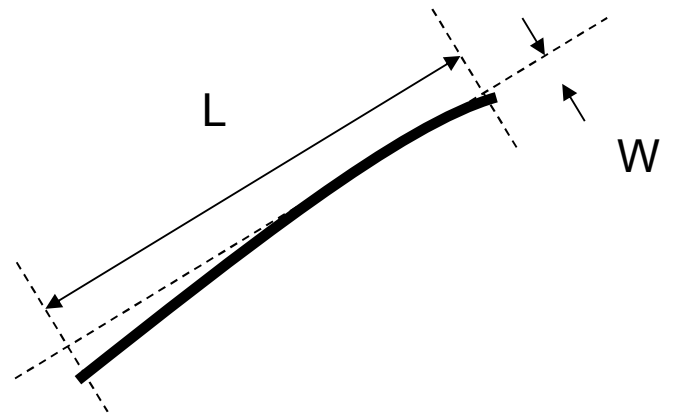
9.1.4.2. Functional Inspection:

Item	Judgment Criteria			Classification
	Area(Note1)	I	O	
Point Defect	Bright dot	Random	2	
		2 dots adjacent	0	0
		3 dots adjacent or more	0	0
	Dark dot	Random	3	
		2 dots adjacent	1	
		3 dots adjacent or more	0	0
	Total Dot Defect		5	
	Distance	Distance between Bright and Bright dot	$L \geq 5\text{mm}$	
		Distance between Bright and Dark dot	$L \geq 5\text{mm}$	
		Distance between Dark dot	$L \geq 5\text{mm}$	
(1) It is defined as Point Defect if defect area > 0.5dot (2) It is ignored if defect area $\leq 0.5\text{dot}$ (3)Weak point defect will be defined as Bright Dot if it can be observed through ND filter 5%( Full Screen Black Inspection)				
Line Defect	Obvious vertical or horizontal line defect is not allowed.			MA
Mura	Not allowed if it can be observed through ND Filter 5 %			MI
Foreign Material in spot shape *Note-3	$D \leq 0.2\text{mm}$ : Ignored $0.2\text{mm} < D \leq 0.5\text{mm}$ : $N \leq 8$ $D > 0.5\text{mm}$ : Not allowed			MI
Foreign Material in line or spiral shape *Note-4	$W \leq 0.05\text{mm}$ or $L \leq 5\text{mm}$ : Ignored $0.05\text{mm} < W \leq 0.2\text{mm}$ and $L 1.0\text{mm} \leq 5\text{mm}$ : $N \leq 8$ $W > 0.2\text{mm}$ or $L > 5\text{mm}$ : Not allowed			MI
Display Function Abnormal	No Malfunction can be allowed			MA

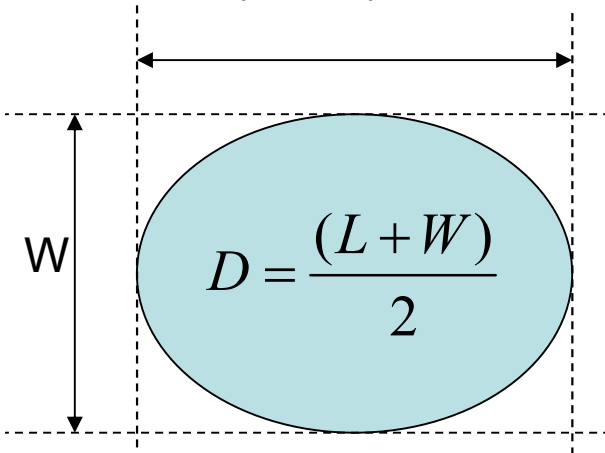
Note-1 : I/O Area Definition



Note-2 : Polarizer Scratch



Note-3 : Spot Foreign Material  
( $W \geq L / 4$ )



Note-4 : Line or Spiral Foreign Material  
( $W < L / 4$ )

