

### Product Summary

$V_{RRM}$	650 V
$I_F (T_C=135^\circ\text{C})$	4 A
$Q_C$	12 nC

### Features

- Extremely low reverse current
- No reverse recovery current
- Temperature independent switching
- Positive temperature coefficient on  $V_F$
- Excellent surge current capability
- Low capacitive charge

### Benefits

- Essentially no switching losses
- System efficiency improvement over Si diodes
- Increased power density
- Enabling higher switching frequency
- Reduction of heat sink requirements
- System cost savings due to smaller magnetics
- Reduced EMI

### Applications

- Switch mode power supplies (SMPS)
- Uninterruptible power supplies
- Motor drivers
- Power factor correction

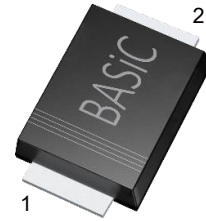
### Package Pin Definitions

- Pin1 - Cathode
- Pin2 - Anode

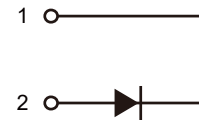
### Package Parameters

Part Number	Marking	Package
B2D04065V1	2465	SMBF

### Package: SMBF



### Electrical Connection



**Maximum Ratings ( $T_c=25^\circ\text{C}$  unless otherwise specified)**

Symbol	Parameter	Test conditions	Value	Unit
$V_{RRM}$	Repetitive peak reverse voltage		650	V
$V_{RSM}$	Non-repetitive peak reverse voltage		650	V
$I_F$	Continuous forward current	$T_{Case}=25^\circ\text{C}$ (1)	10	A
		$T_{PCB}=25^\circ\text{C}$ (2)	6	
		$T_{Case}=135^\circ\text{C}$ (1)	4	
		$T_{PCB}=100^\circ\text{C}$ (2)	4	
$I_{FSM}$	Non-repetitive forward surge current	$T_c=25^\circ\text{C}$ , $t_p=10\text{ms}$ , Half sine wave	32	A
$\int i^2 dt$	$i^2 t$ value	$T_c=25^\circ\text{C}$ , $t_p=10\text{ms}$	5.12	A <sup>2</sup> S
$P_{tot(case)}^{(1)}$	Power dissipation	$T_c=25^\circ\text{C}$	25	W
		$T_c=110^\circ\text{C}$	10	
$P_{tot(PCB)}^{(2)}$	Power dissipation	$T_c=25^\circ\text{C}$	12	W
		$T_c=110^\circ\text{C}$	5	
$T_j$	Operating junction temperature		-55~175	$^\circ\text{C}$
$T_{stg}$	Storage temperature		-55~175	$^\circ\text{C}$

(1) This value is rated according to  $R_{th(case)}$

(2) This value is rated according to  $R_{th(PCB)}$

**Thermal Characteristics**

Symbol	Parameter	Value			Unit
		Min.	Typ.	Max.	
$R_{th(case)}$	Thermal resistance from junction to case		6		K/W
$R_{th(PCB)}^{(1)}$	Thermal resistance from junction to PCB		12.13		K/W

(1) When mounted on a 1-inch<sup>2</sup> FR-4, 2 Oz copper board,  $t < 10$  s

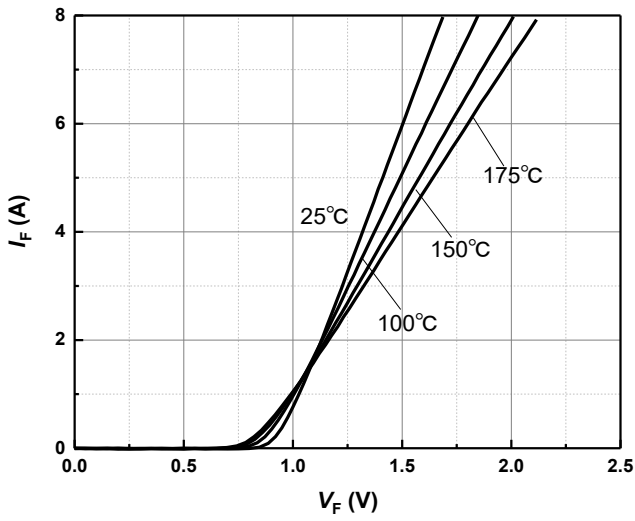
**Electrical Characteristics**  
**Static Characteristics**

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$V_{DC}$	DC blocking voltage	$T_J=25^{\circ}C$	650			V
$V_F$	Diode forward voltage	$I_F=4A$ $T_J=25^{\circ}C$ $I_F=4A$ $T_J=175^{\circ}C$		1.31 1.5	1.6 2	V
$I_R$	Reverse current	$V_R=650V$ $T_J=25^{\circ}C$ $V_R=650V$ $T_J=175^{\circ}C$		1 10	60 100	$\mu A$

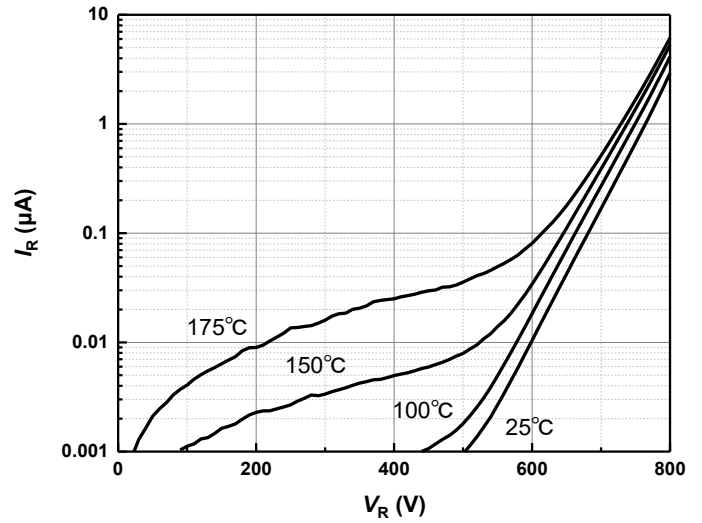
**AC Characteristics**

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$Q_C$	Total capacitive charge	$V_R=400V$ $T_J=25^{\circ}C$ $Q_C=\int_0^{V_R} C(V)dV$		12		nC
$C$	Total capacitance	$V_R=1V$ $f=1MHz$ $V_R=300V$ $f=1MHz$ $V_R=600V$ $f=1MHz$		183 21 20.5		pF
$E_C$	Capacitance stored energy	$V_R=400V$		3		$\mu J$

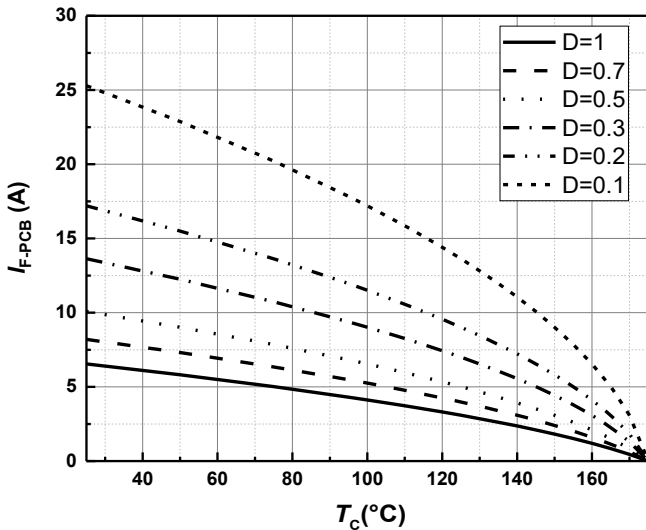
**Typical Performance**



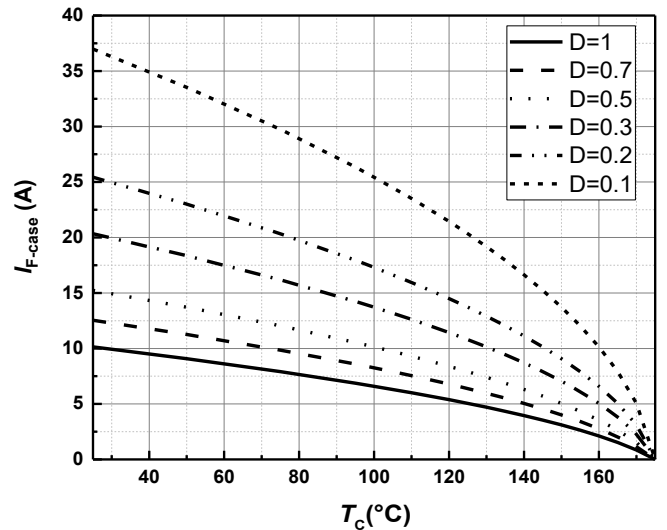
**Figure 1** Typical forward characteristics



**Figure 2** Typical reverse current as function of reverse voltage

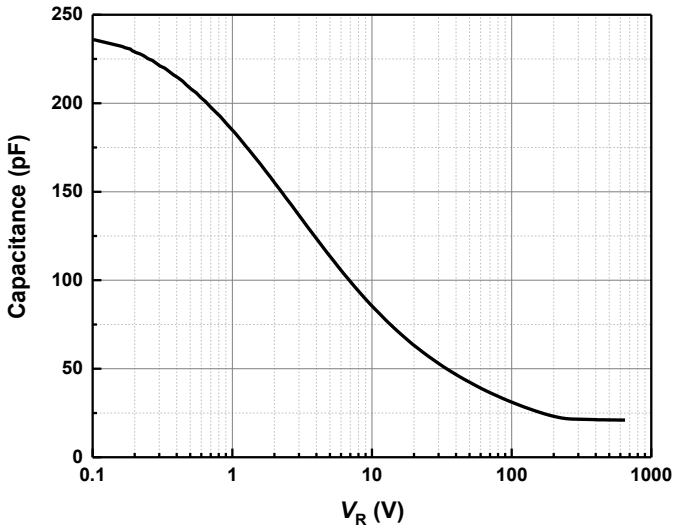


**Figure 3** Diode forward current ( $I_{F-PCB}$ ) as function of temperature, D=duty cycle

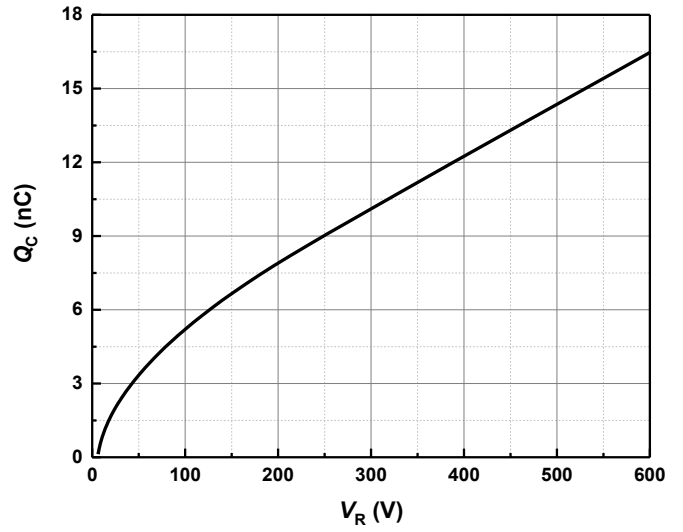


**Figure 4** Diode forward current ( $I_{F-case}$ ) as function of temperature, D=duty cycle

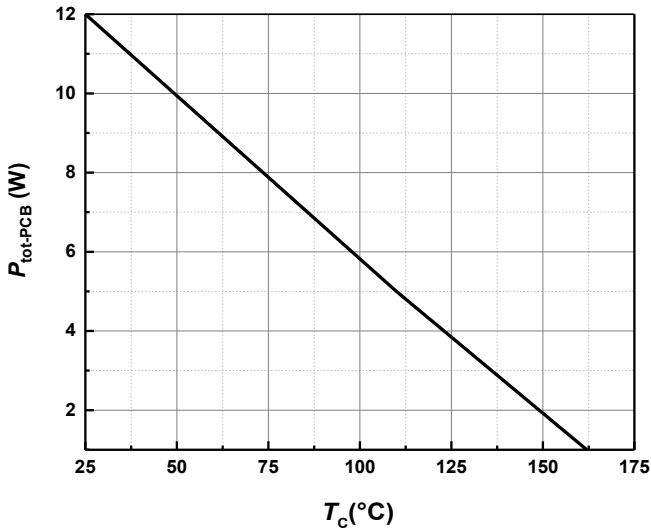
**Typical Performance**



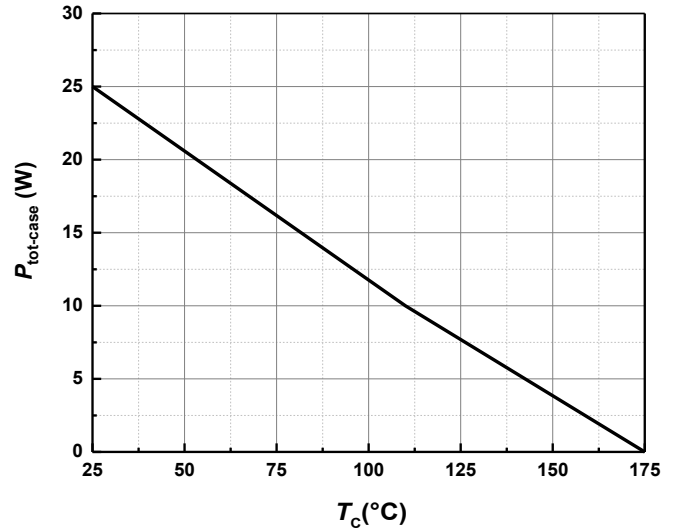
**Figure 5** Typical capacitance as function of reverse voltage,  $C=f(V_R)$ ;  $T_j=25^\circ\text{C}$ ;  $f=1\text{ MHz}$



**Figure 6** Typical reverse charge as function of reverse voltage

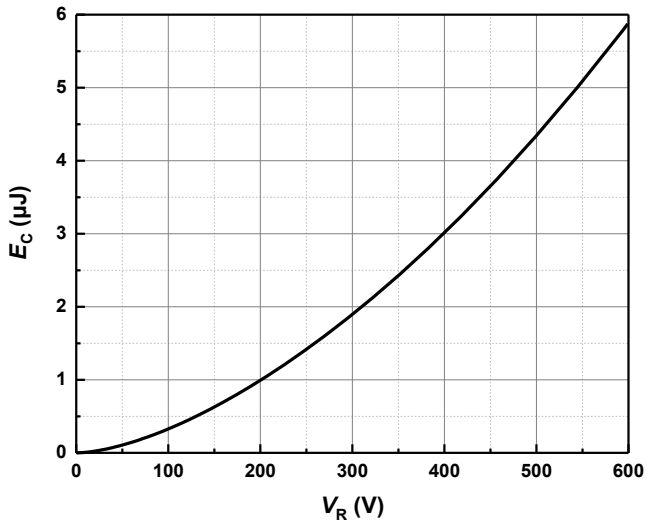


**Figure 7** Power dissipation ( $P_{\text{tot-PCB}}$ ) as function of case temperature

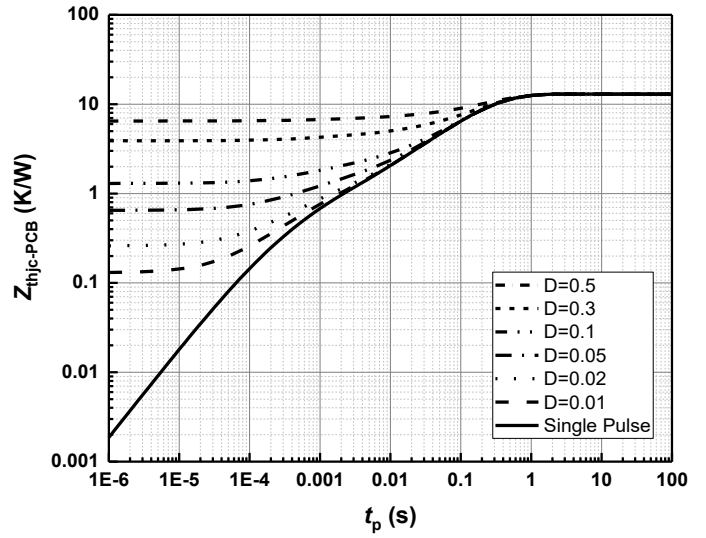


**Figure 8** Power dissipation ( $P_{\text{tot-case}}$ ) as function of case temperature

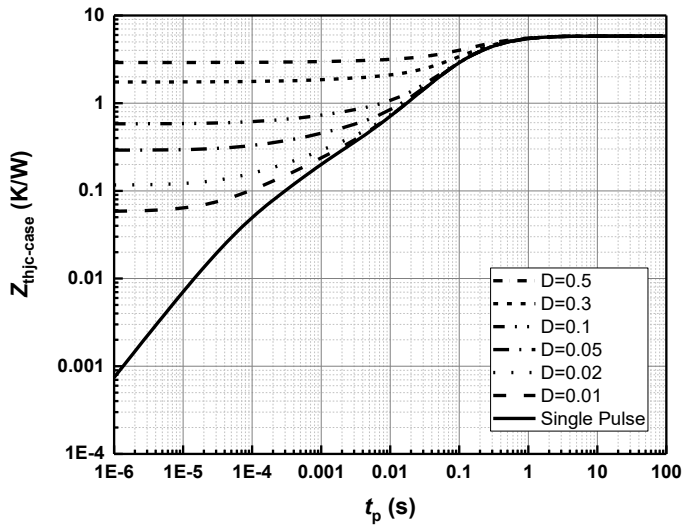
**Typical Performance**



**Figure 9** Capacitance stored energy

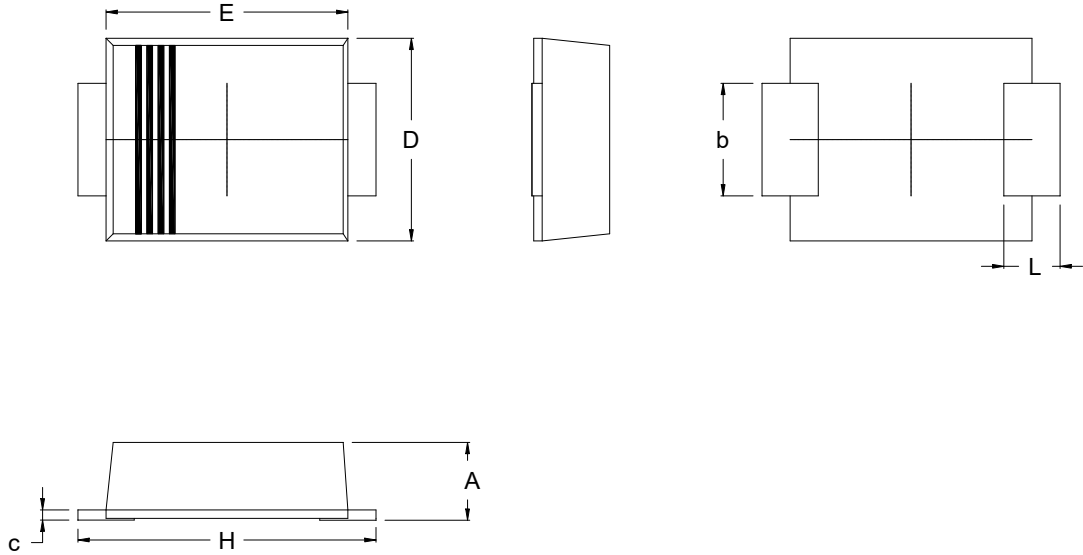


**Figure 10** Max. transient thermal impedance,  $Z_{thjc-PCB} = f(t)$ , parameter:  $D = t / T$



**Figure 11** Max. transient thermal impedance,  $Z_{thjc-case} = f(t)$ , parameter:  $D = t / T$

Package Dimensions



SYMBOL	mm		
	MIN	NOM	MAX
A	1.30	1.35	1.40
b	1.98	2.00	2.02
c	0.12	0.15	0.18
D	3.55	3.60	3.65
E	4.25	4.30	4.35
H	5.20	5.30	5.40
L	0.70	-	1.02

**Revision History**

<b>Document Version</b>	<b>Date of Release</b>	<b>Description of Changes</b>
Rev 0.0	2022-07-22	Release of the preliminary datasheet.

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**Shenzhen, China**  
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